

NanoPi Fire 3/2A

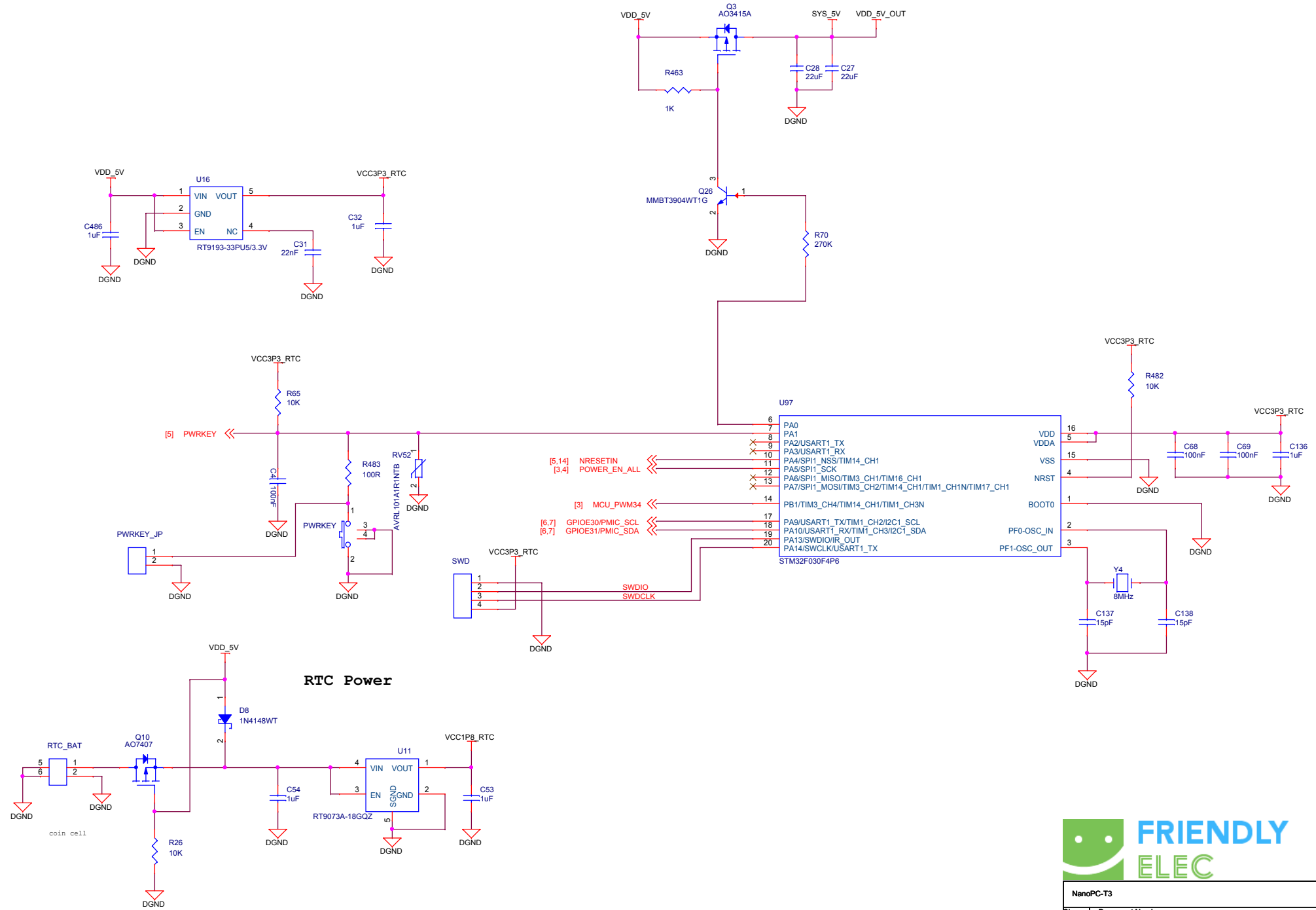
1709

2020/01/13:

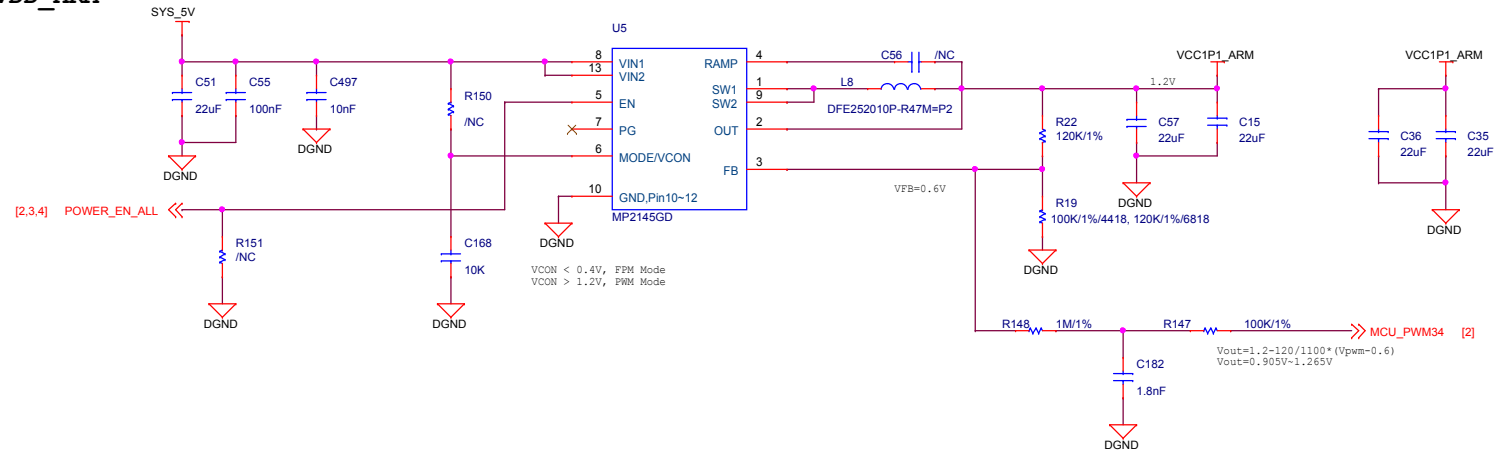
R463 -> 1K, R70 -> 270K, R150 -> NC, C168 -> 10K
for S5P4418 R19 -> 100K/1%



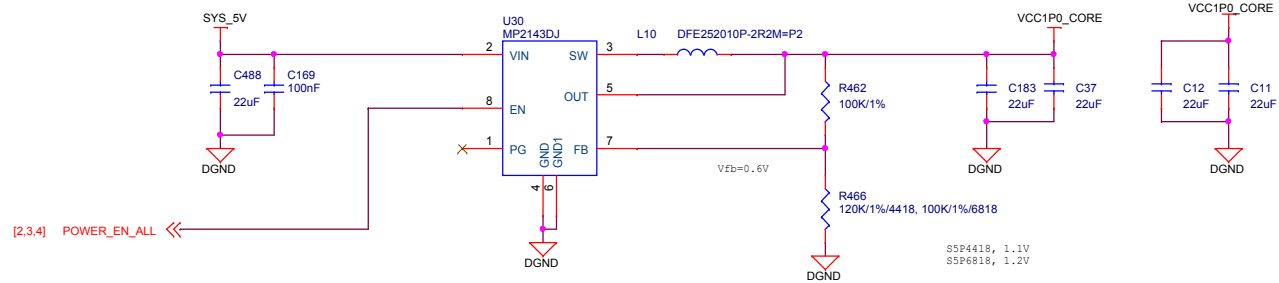
NanoPC-T3		
Size A3	Document Number 01:Title	Rev 1709
Date:	Monday, January 13, 2020	Sheet 1 of 14

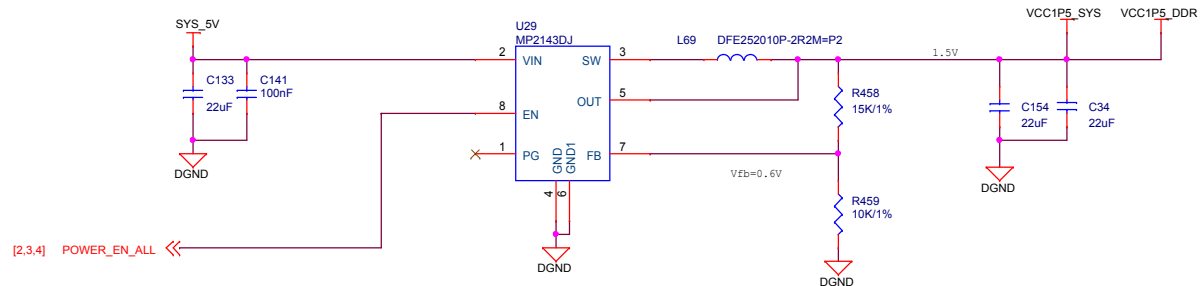


VDD_ARM

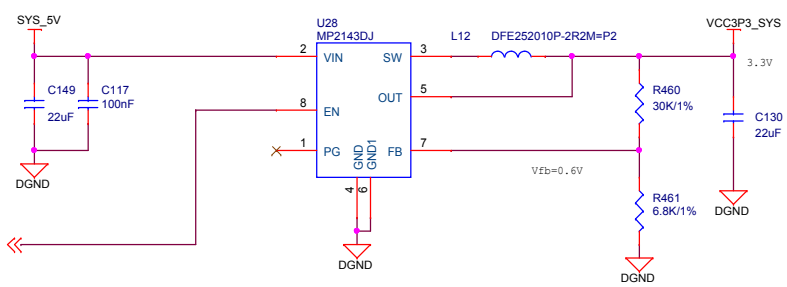


VDD_CORE

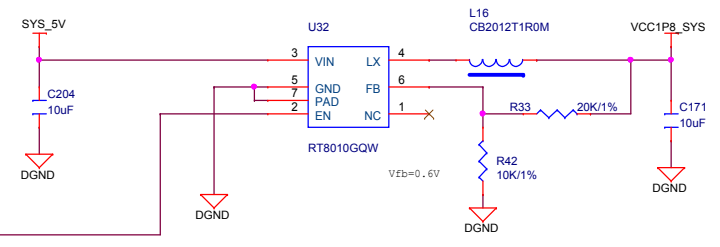




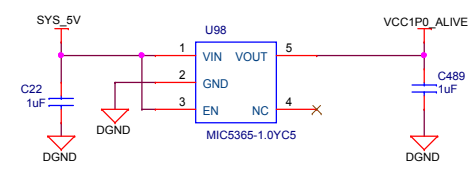
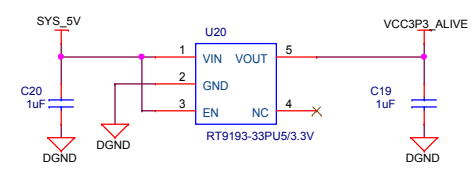
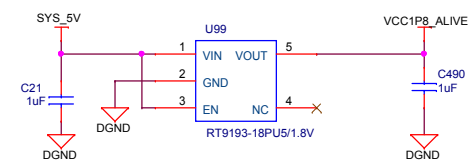
[2,3,4] POWER_EN_ALL <<<



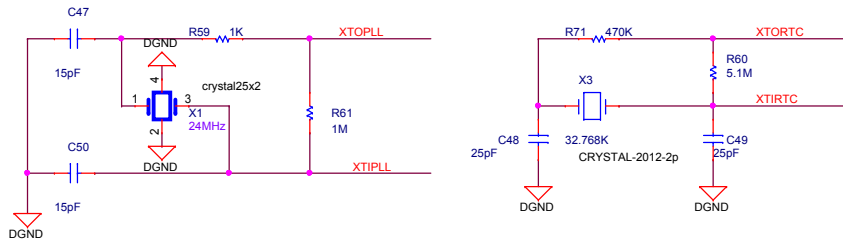
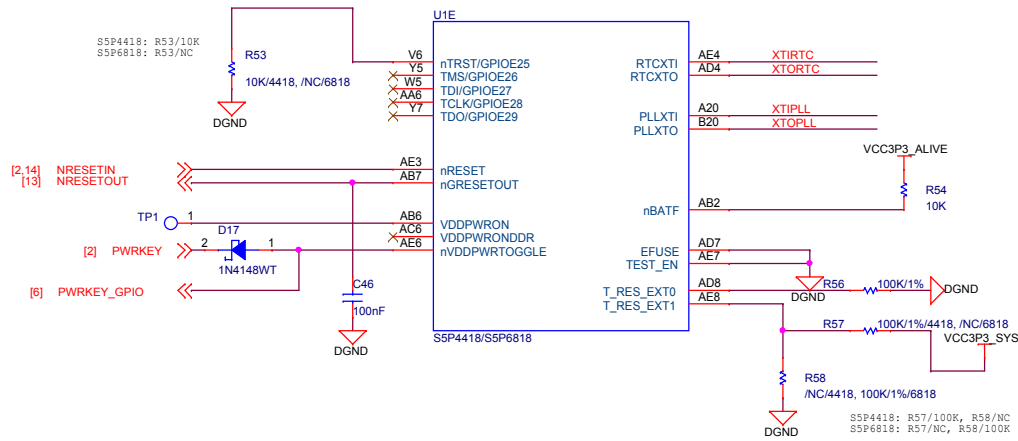
[2,3,4] POWER_EN_ALL <<<



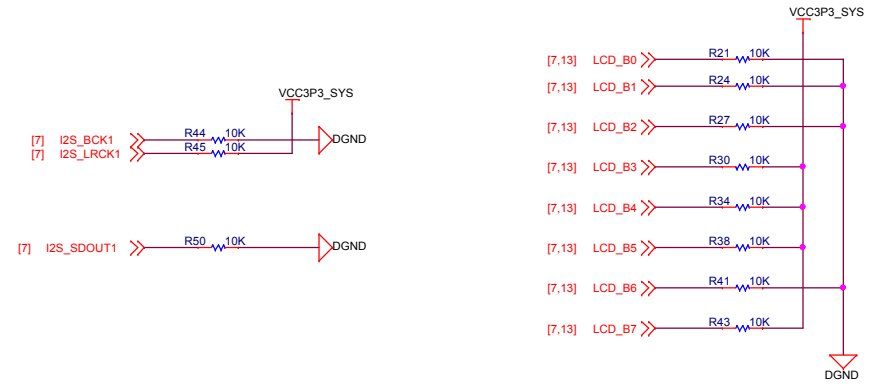
[2,3,4] POWER_EN_ALL <<<



System Reset, Clocks



Boot Mode Config



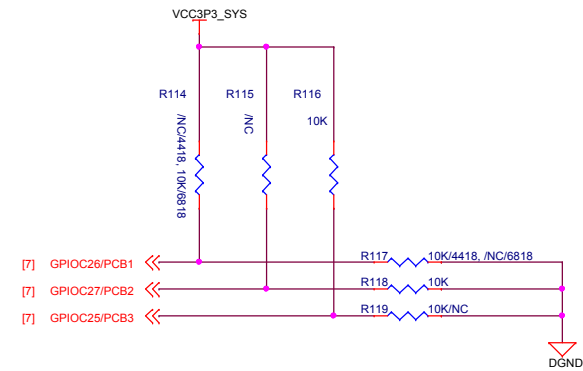
Boot media port select (SPI, eMMC)

	CH0	CH1	CH2
SD3	LOW	HIGH	LOW
CAM1_D3	LOW	LOW	HIGH

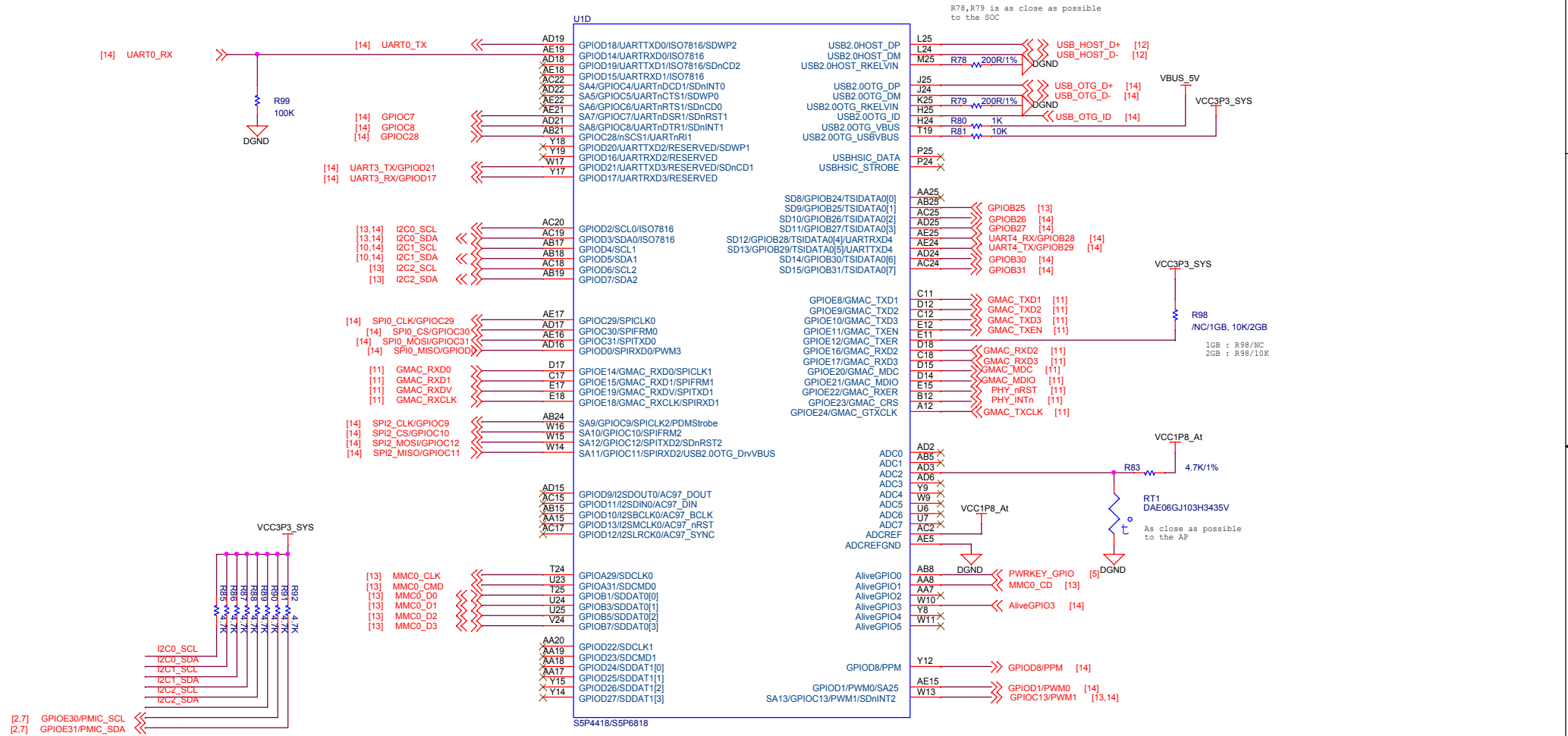
BOOT MODE OPTION

	eMMC	SPI	USB	NAND
SD0	HIGH	LOW	LOW	HIGH
SD1	LOW	LOW	HIGH	HIGH
SD2	HIGH	HIGH	HIGH	HIGH
SD4	LOW	HIGH		
SD5	LOW	LOW		

PCB Version



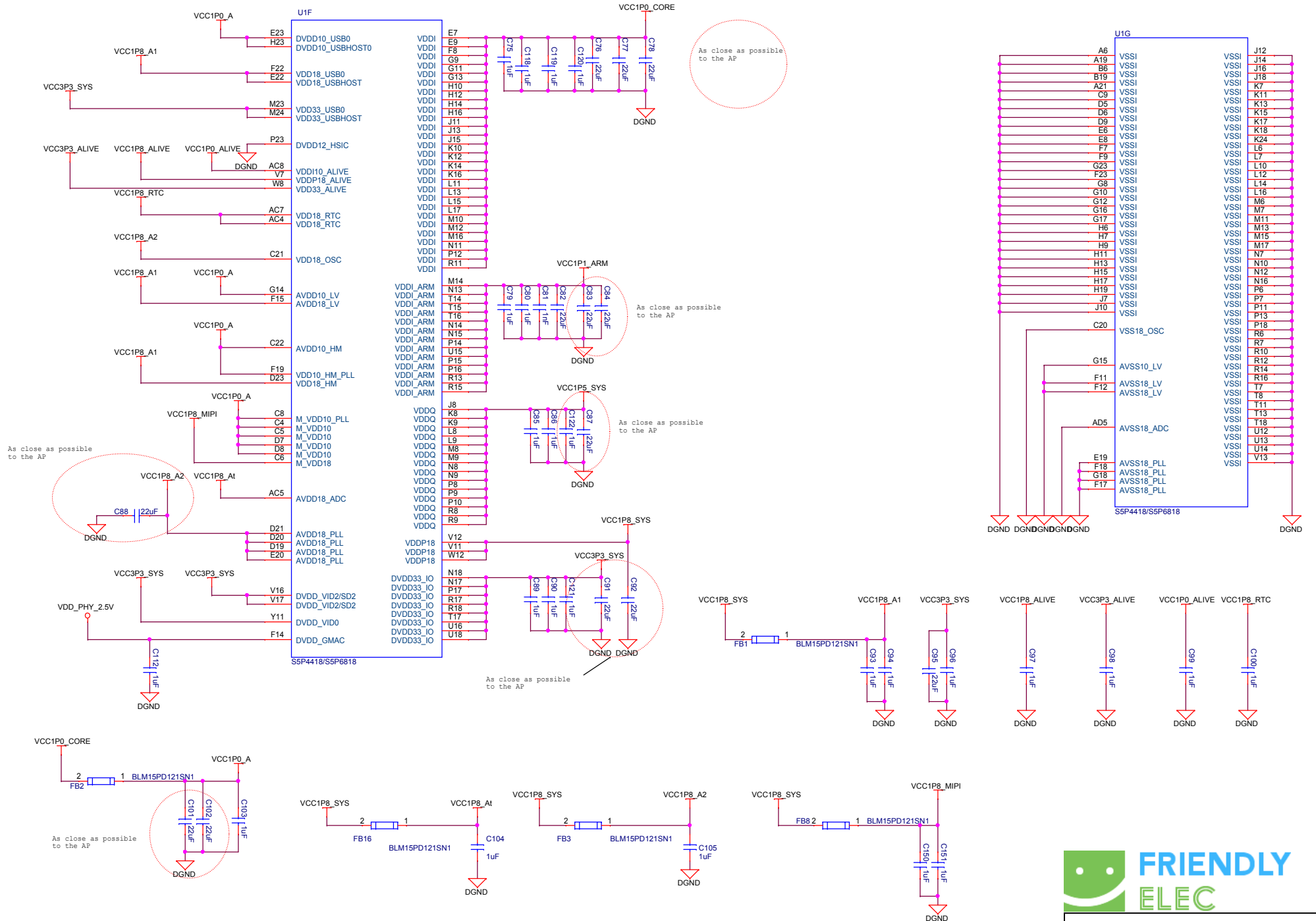
AP Peripherals



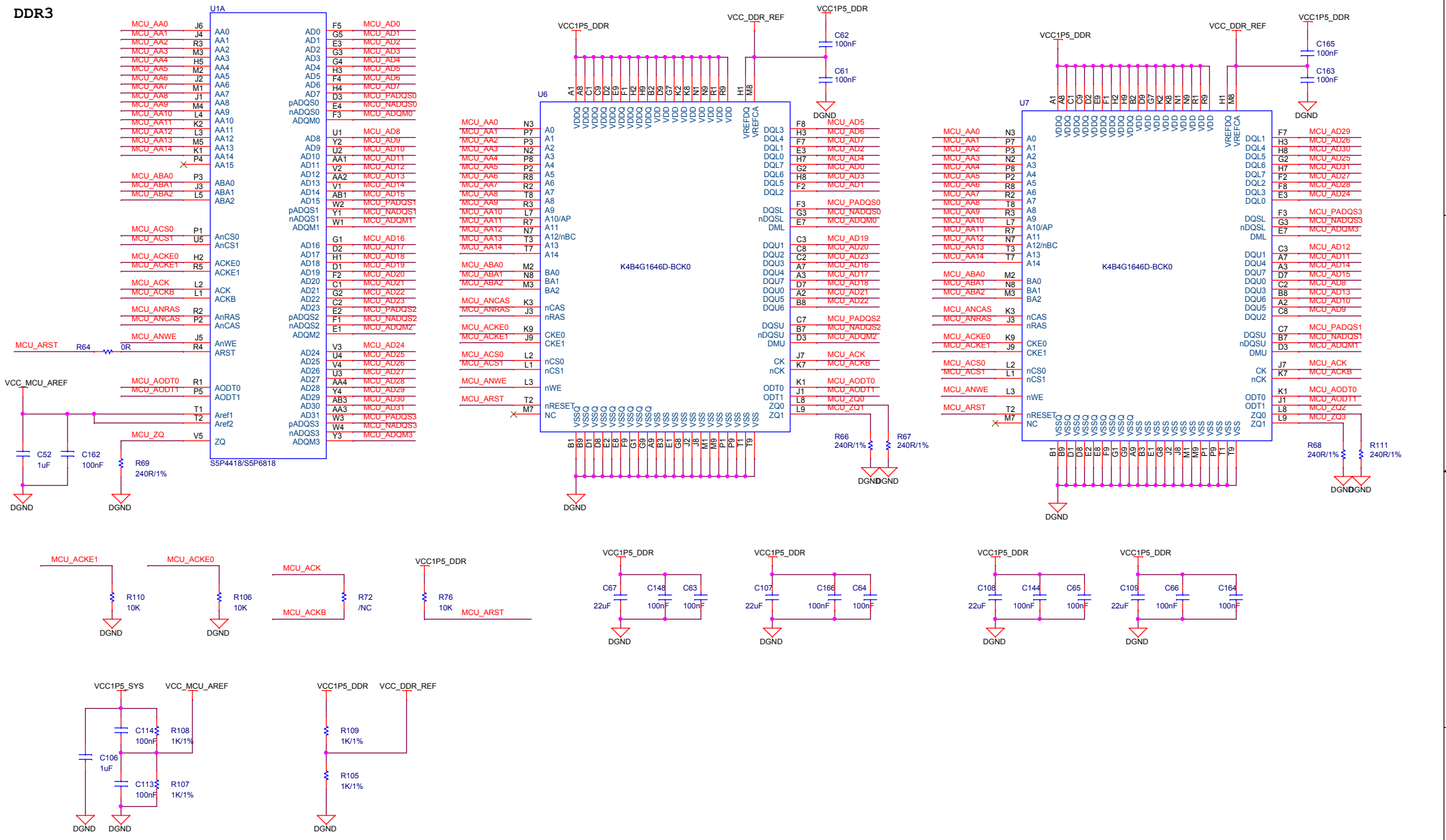
I2C CH0 : Camera
 I2C CH1 : HDMI EDID
 I2C CH2 : Touch
 PMIC_I2C : PMIC



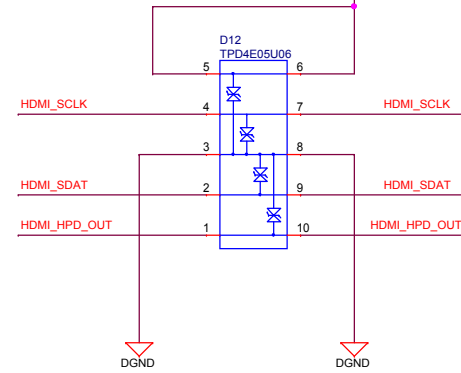
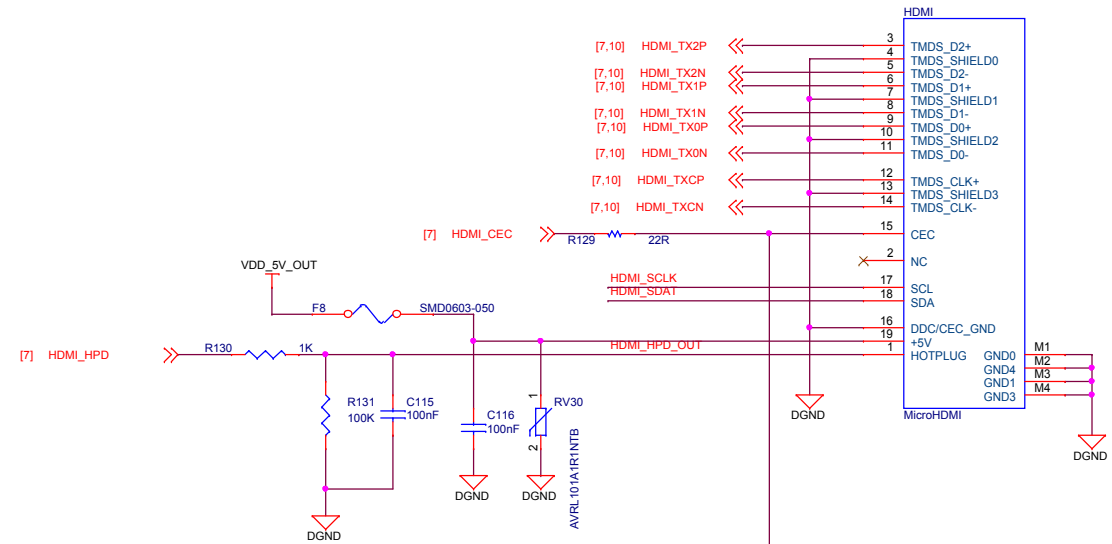
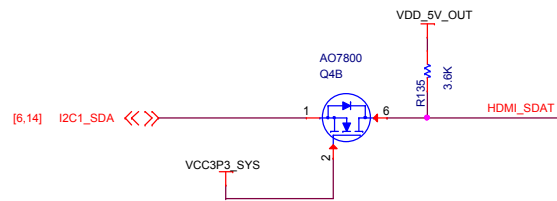
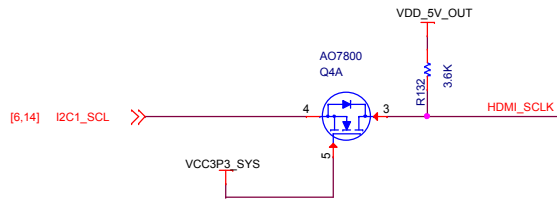
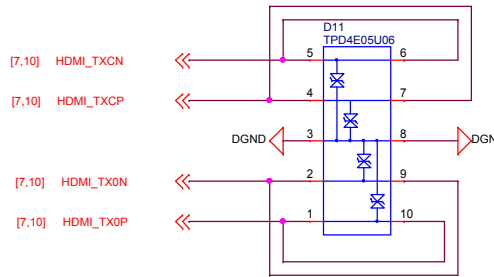
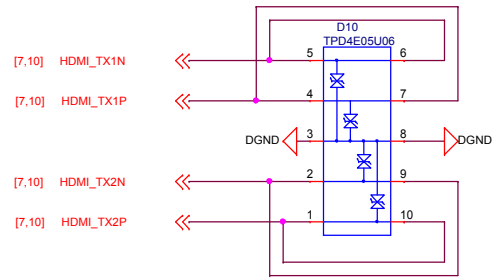
AP Power



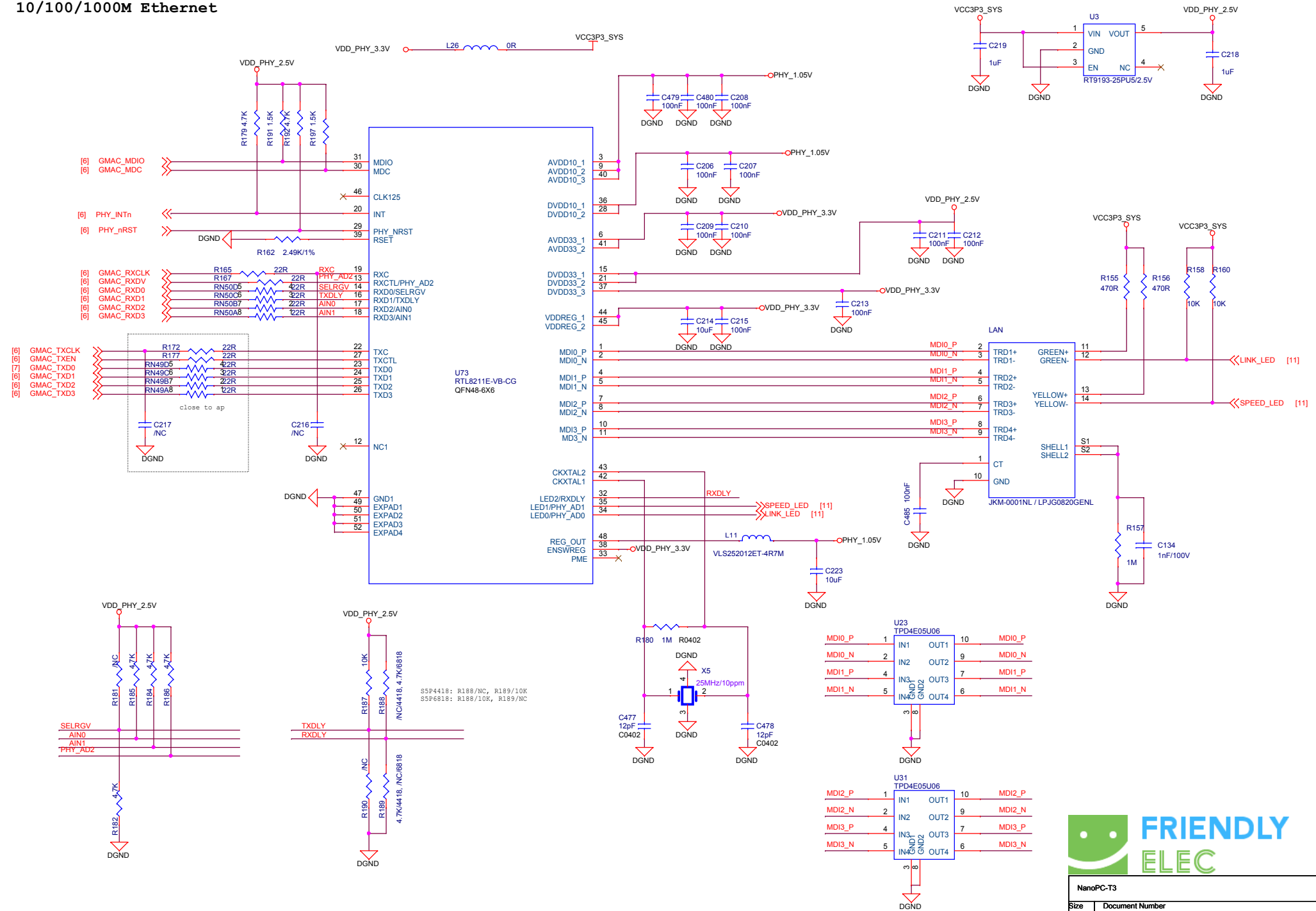
DDR3



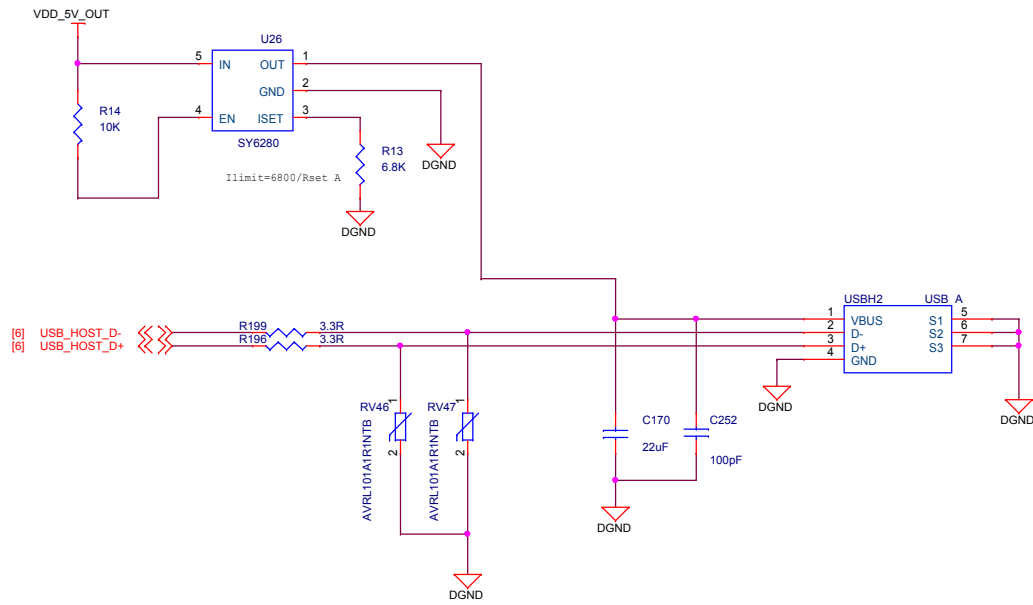
HDMI



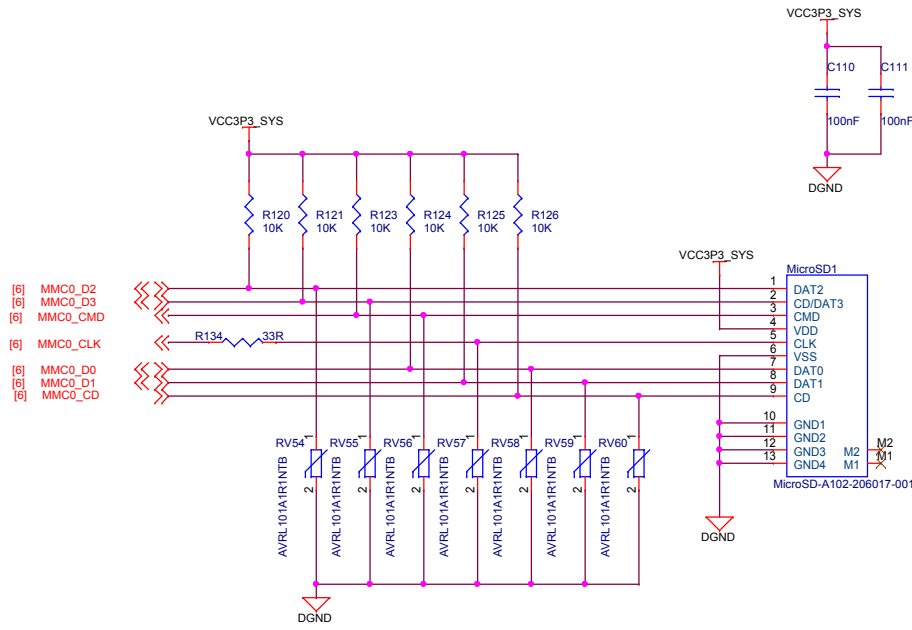
10/100/1000M Ethernet



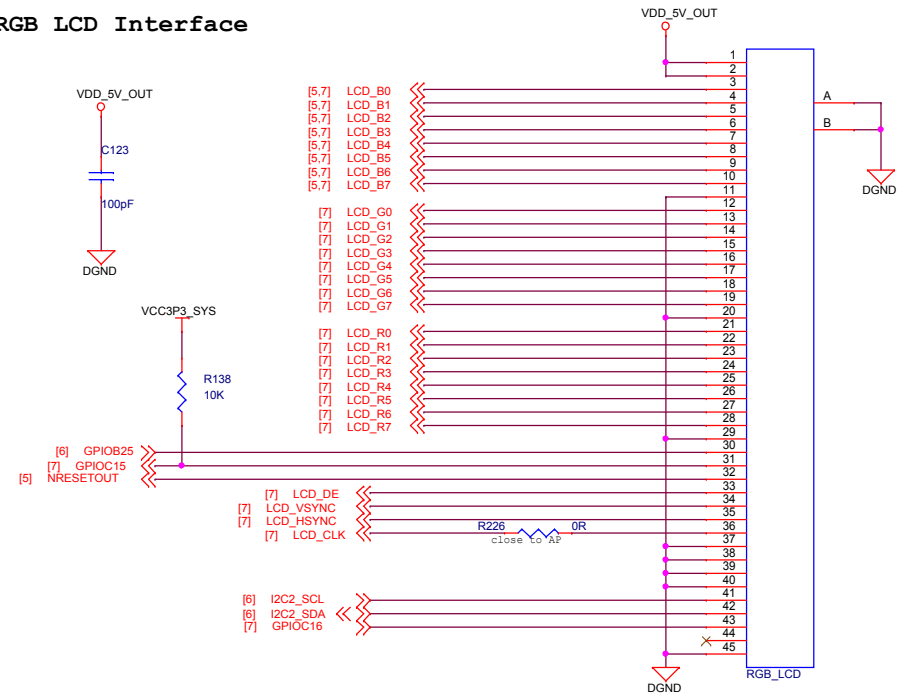
USB 2.0 Host



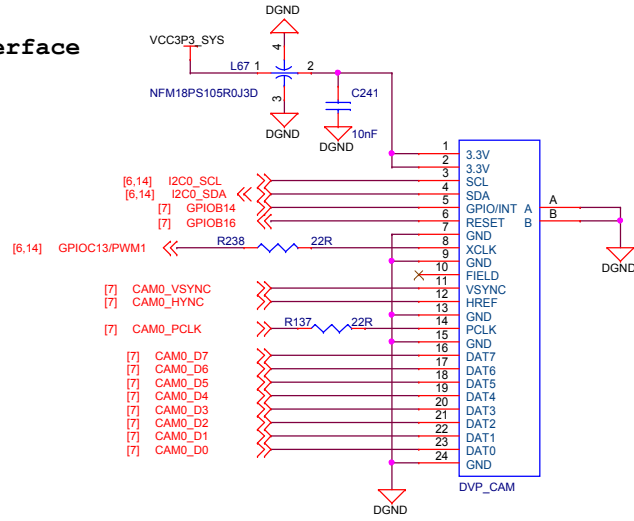
SDCARD



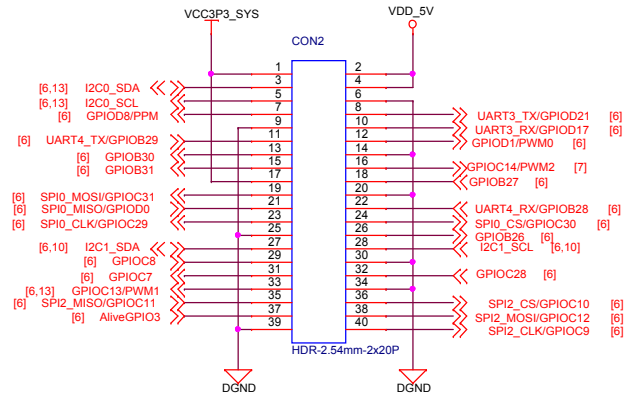
RGB LCD Interface



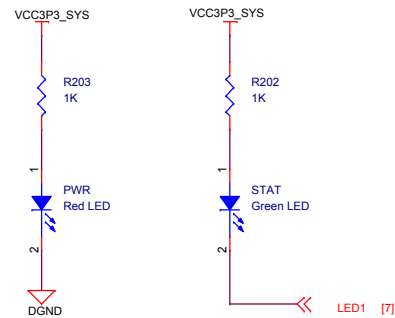
Camera Interface



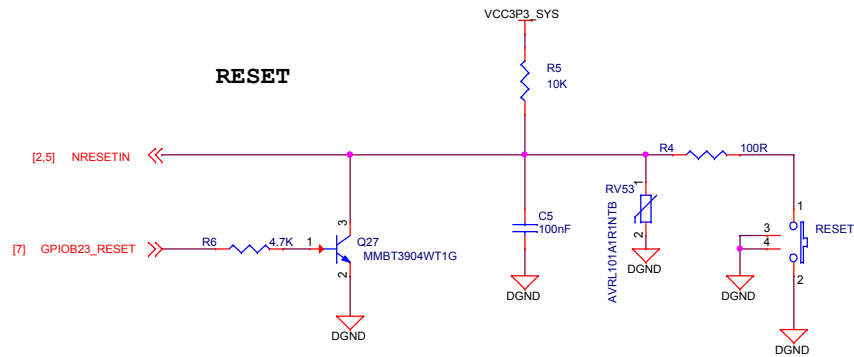
2.54mm Header



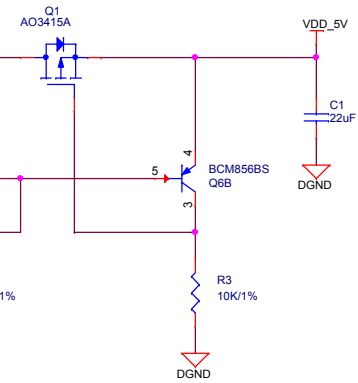
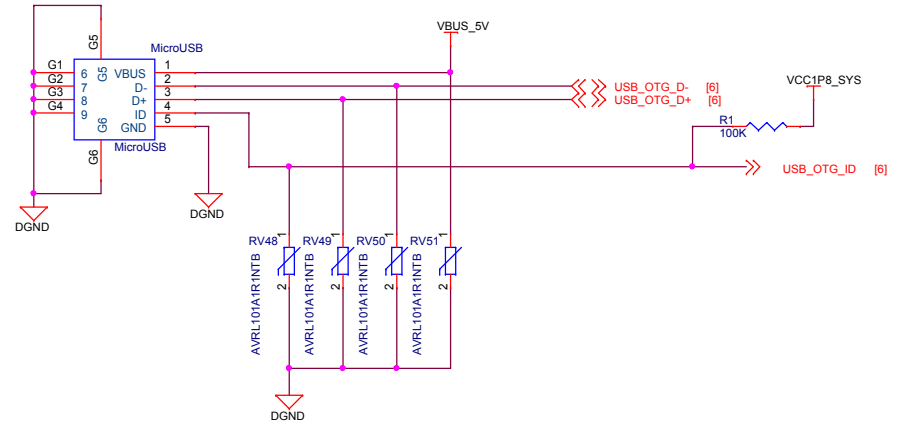
LEDs



RESET



MicroUSB



Debug UART

