

Num	Description	Num	Description
1		6	
2		7	
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## KEY

KEY\_UP >>> KEY\_UP  
KEY\_DOWN >>> KEY\_DOWN  
KEY\_LEFT >>> KEY\_LEFT  
KEY\_RIGHT >>> KEY\_RIGHT  
KEY\_ENTER >>> KEY\_ENTER

## DIGITAL

DIG[0..7] >>> DIG[0..7]  
SEL[0..5] >>> SEL[0..5]

## LCD

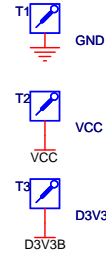
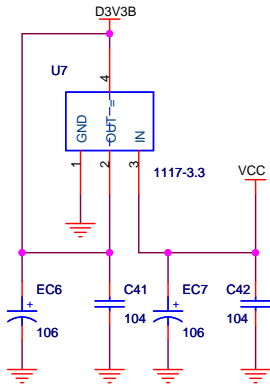
LCD\_CS >>> LCD\_CS  
LCD\_A0 >>> LCD\_A0  
LCD\_SCL >>> LCD\_SCL  
LCD\_SI >>> LCD\_SI

## VGA

VGA\_R >>> VGA\_R  
VGA\_G >>> VGA\_G  
VGA\_B >>> VGA\_B  
VGA\_HS >>> VGA\_HS  
VGA\_VS >>> VGA\_VS

## PS2

PS2DAT >>> PS2DAT  
PS2CLK >>> PS2CLK



## TO NET

LAN\_nINT >>> LAN\_nINT  
LAN\_MISO >>> LAN\_MISO  
LAN\_MOSI >>> LAN\_MOSI  
LAN\_SCK >>> LAN\_SCK  
LAN\_nCS >>> LAN\_nCS  
LAN\_nRST >>> LAN\_nRST  
LAN\_nWOL >>> LAN\_nWOL

## TO RTC 422 232

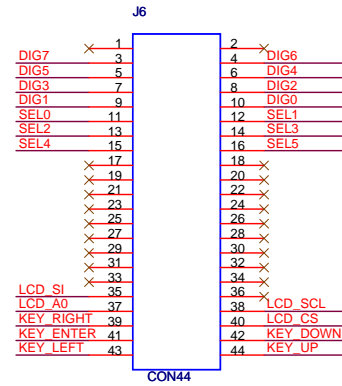
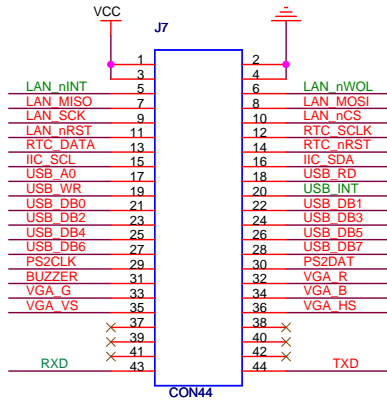
RTC\_SCLK >>> RTC\_SCLK  
RTC\_nRST >>> RTC\_nRST  
RTC\_DATA >>> RTC\_DATA  
RXD >>> RXD  
TXD >>> TXD  
IIC\_SCL >>> IIC\_SCL  
IIC\_SDA >>> IIC\_SDA

## TO USB

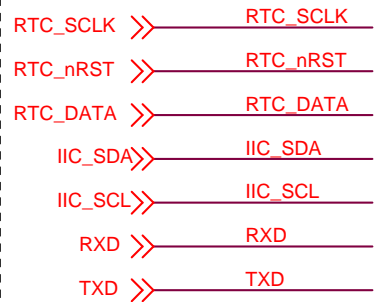
USB\_DB[0..7] >>> USB\_DB[0..7]  
USB\_INT >>> USB\_INT  
USB\_WR >>> USB\_WR  
USB\_RD >>> USB\_RD  
USB\_A0 >>> USB\_A0

## TO BUZZER

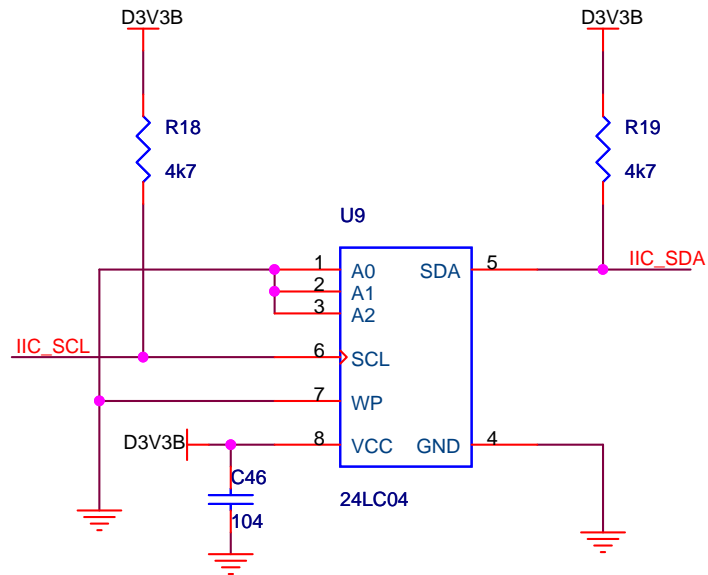
BUZZER >>> BUZZER



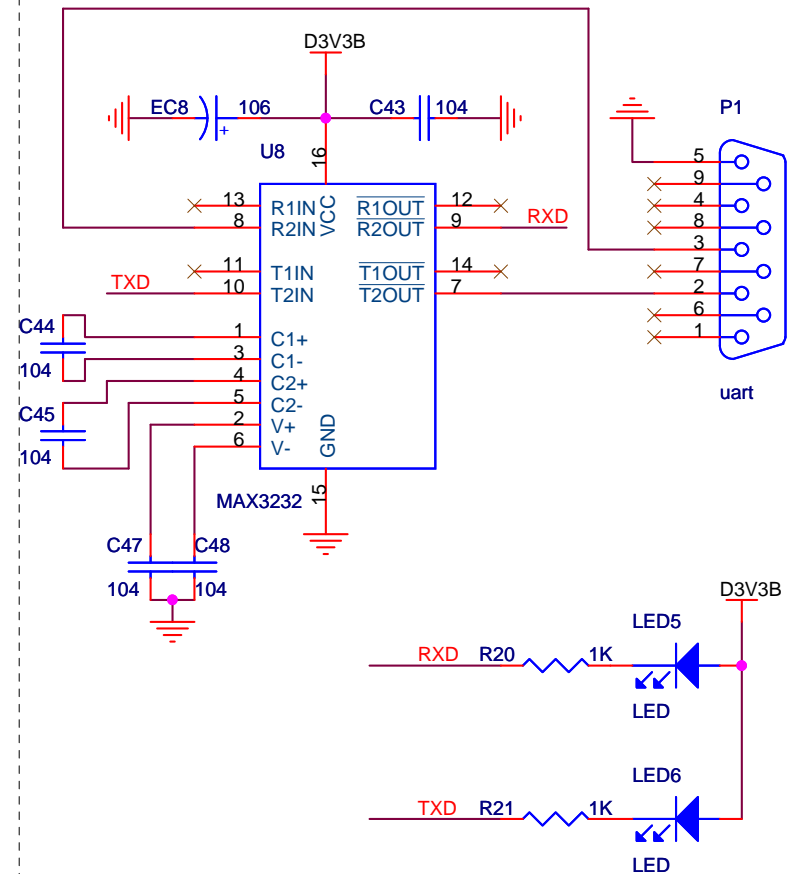
## FROM FPGA



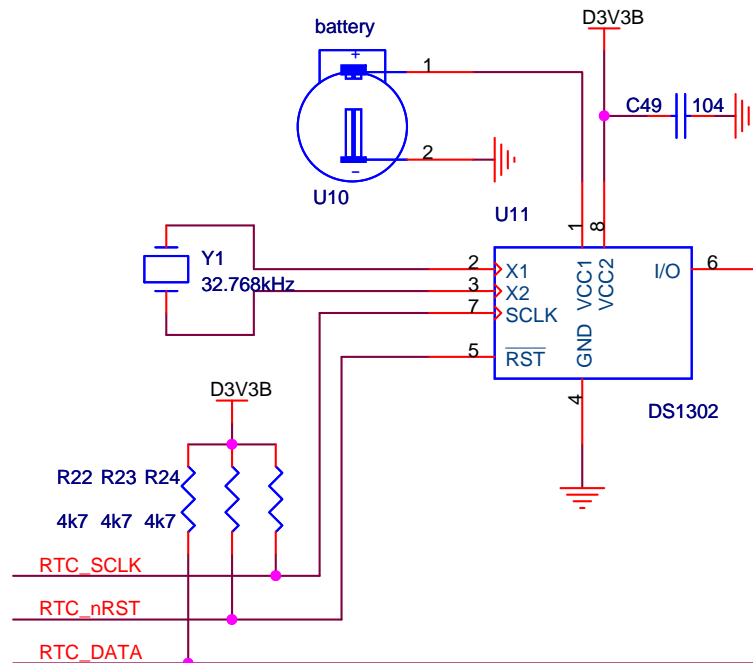
## EEPROM



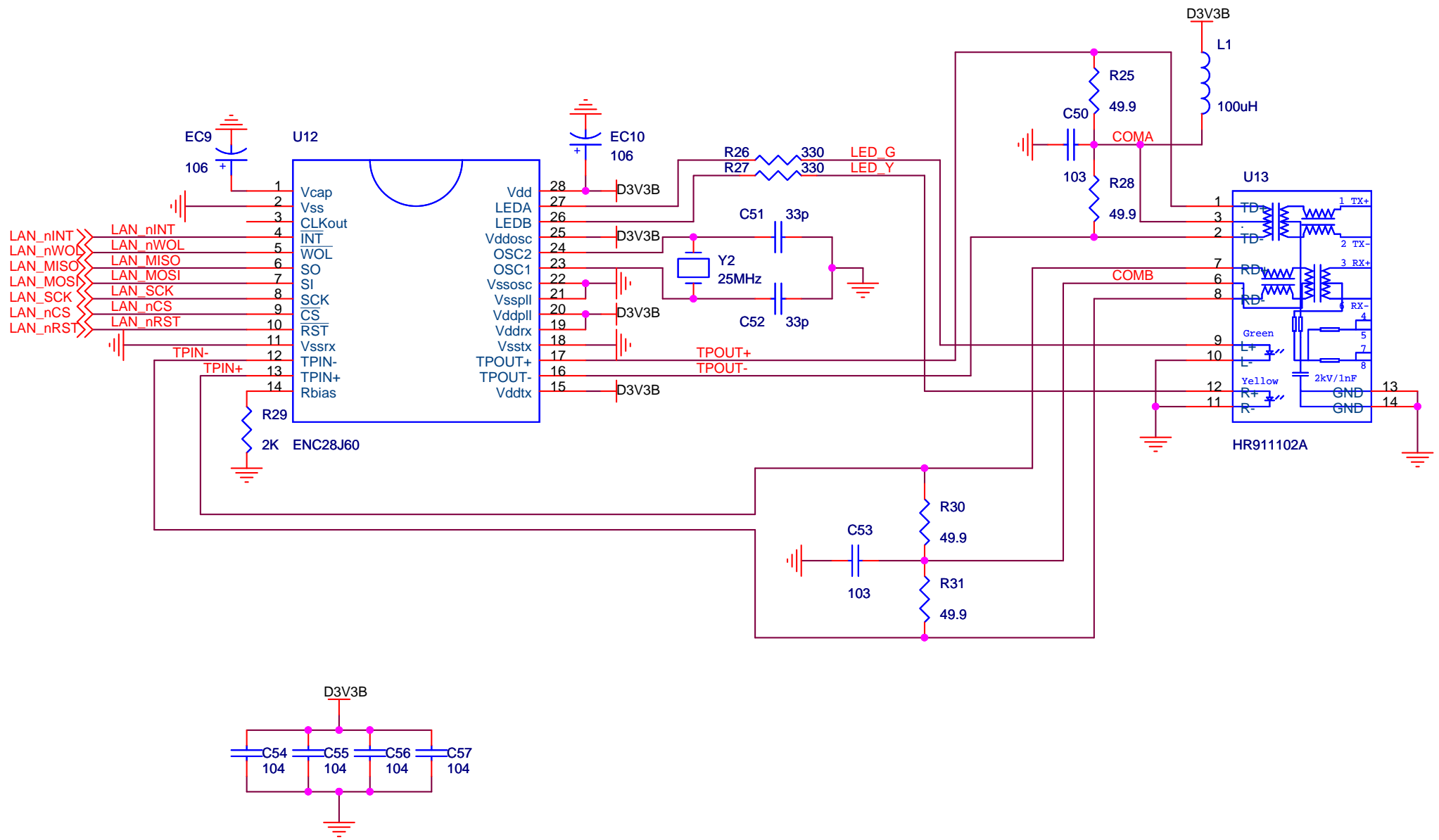
## UART



## RTC

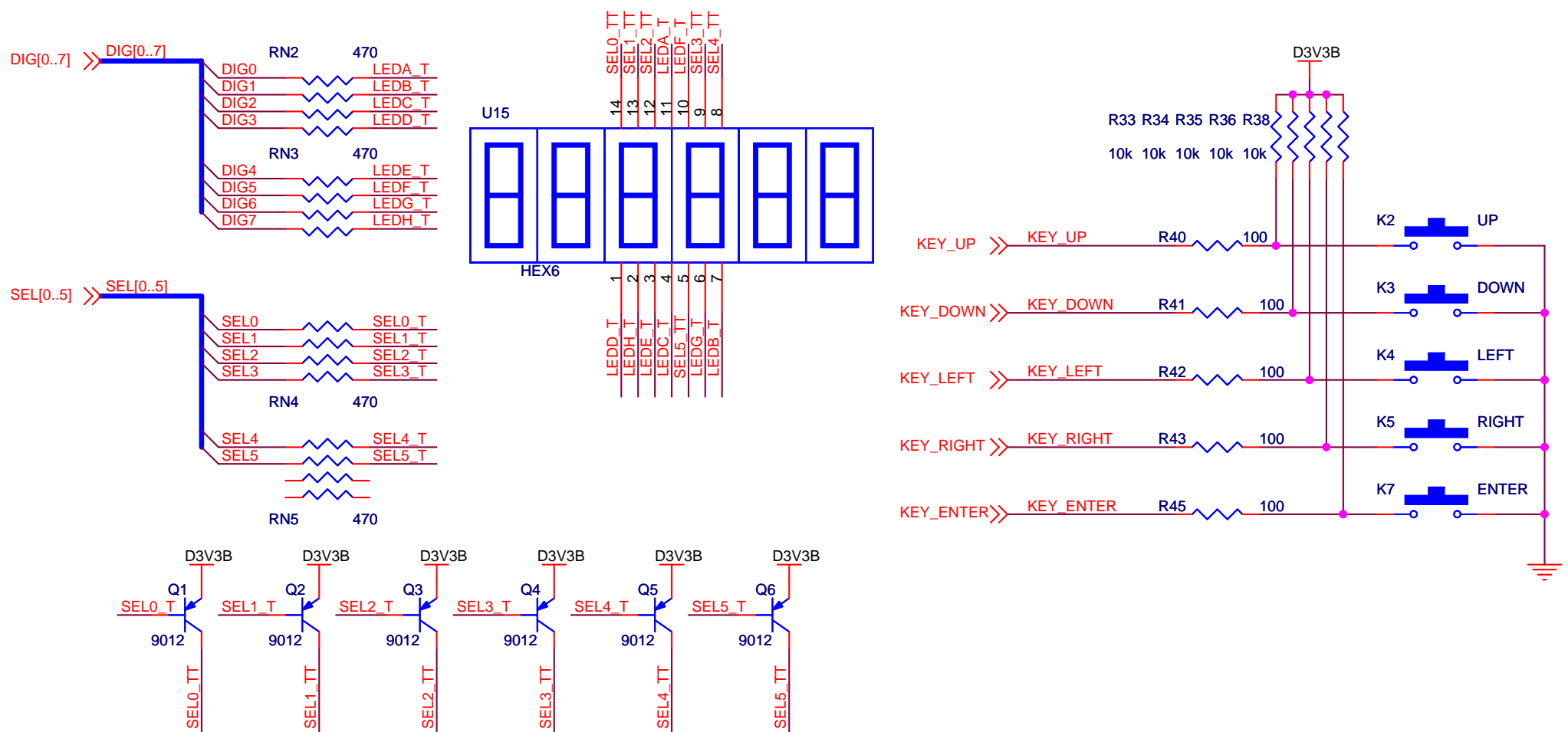


Title		
4_rtc_eeprom_uart		
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5_lan			
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7_led_key_digit		
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