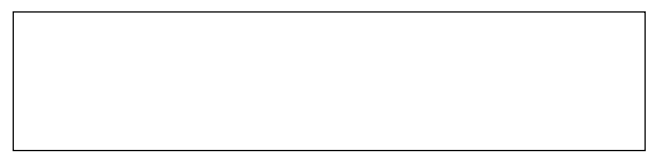
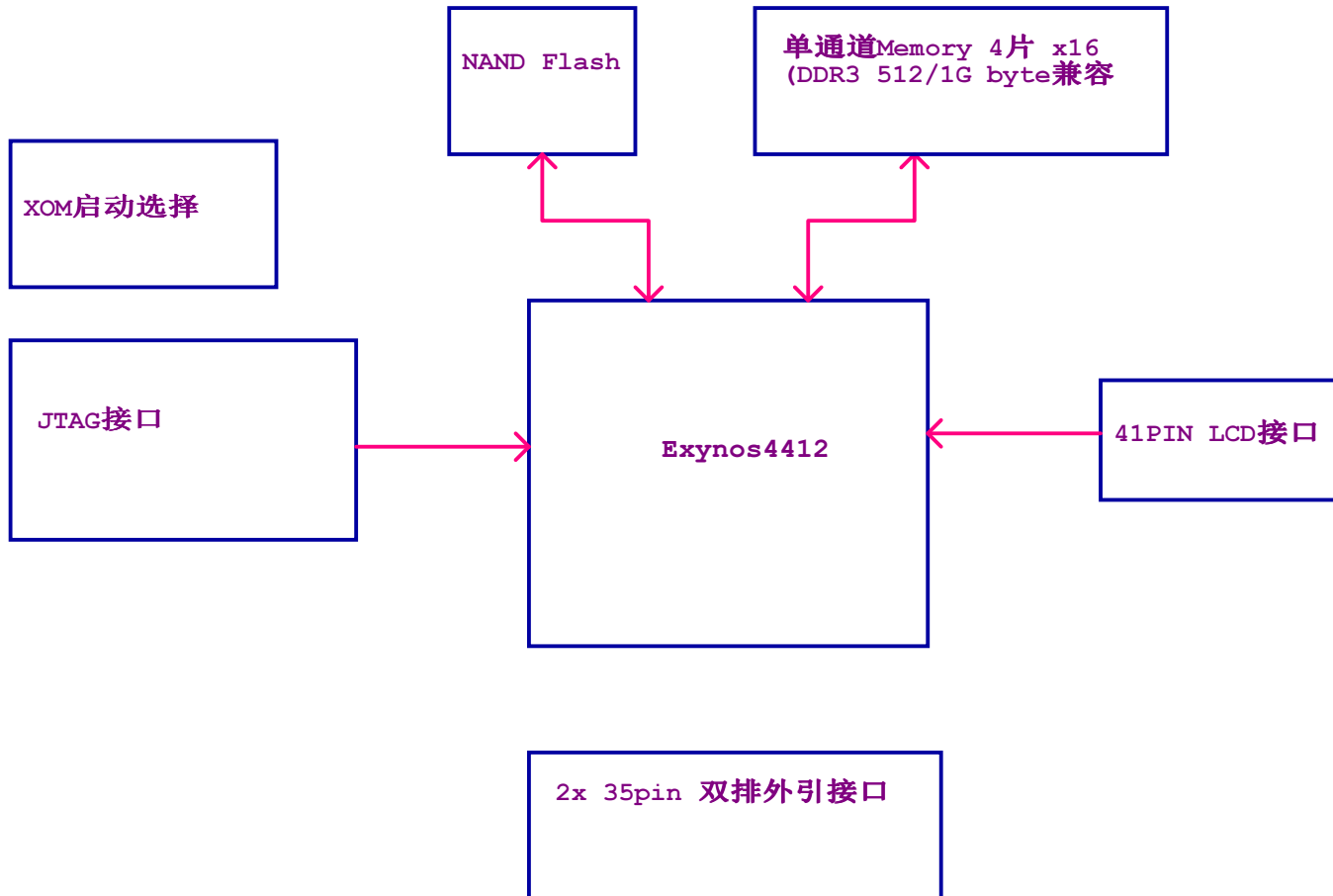


Revision History

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D

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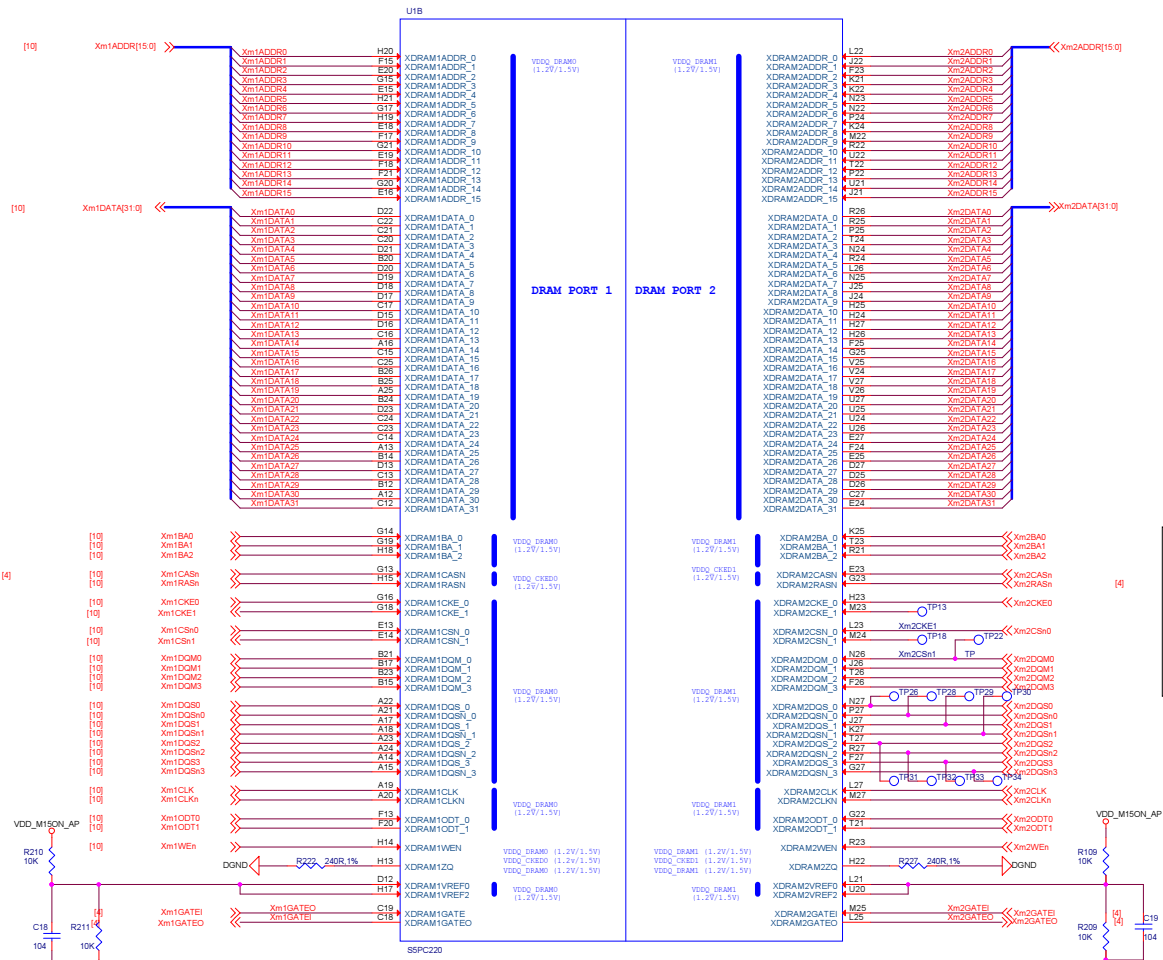
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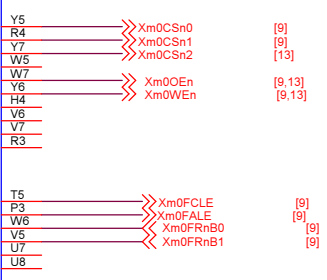
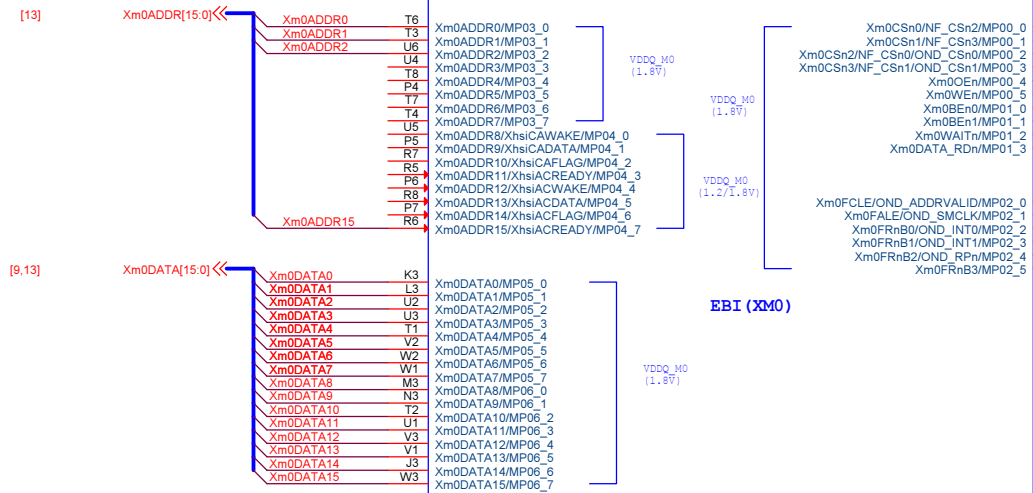


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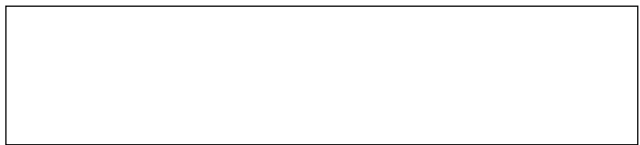
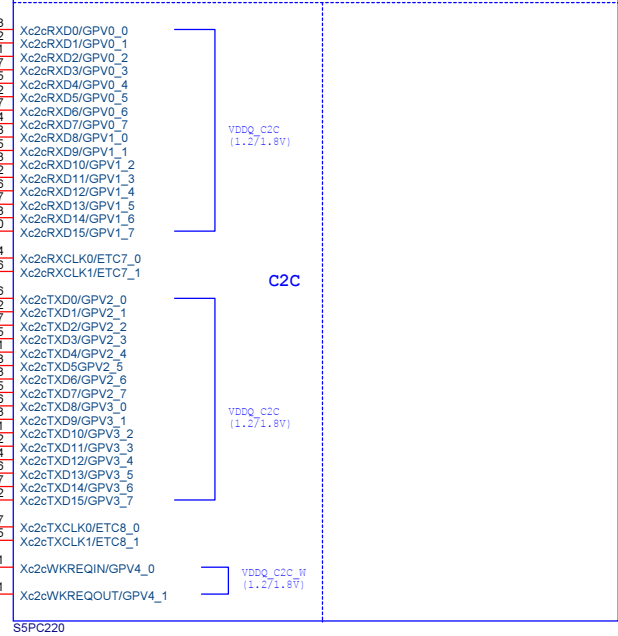
线与clk等长

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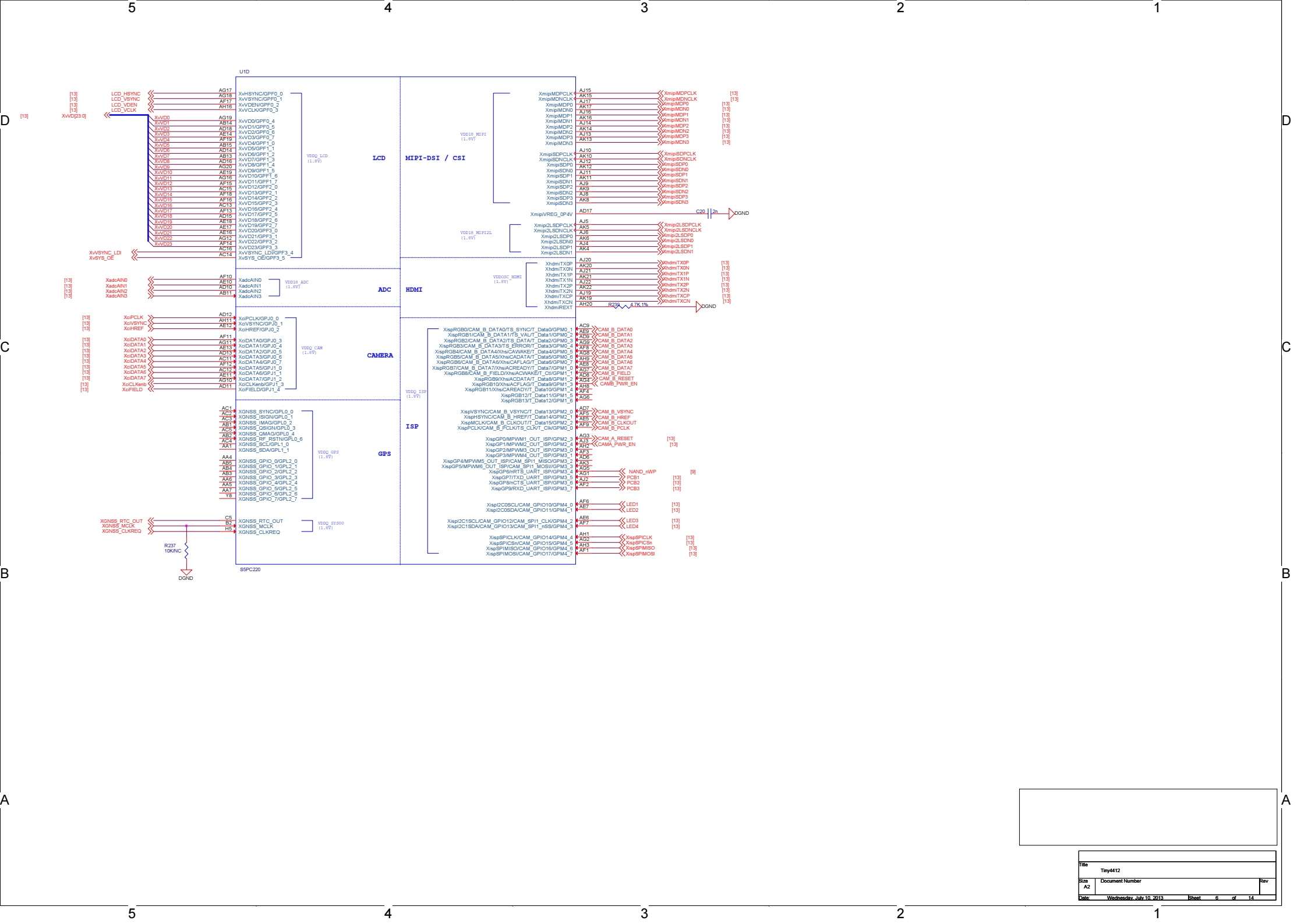
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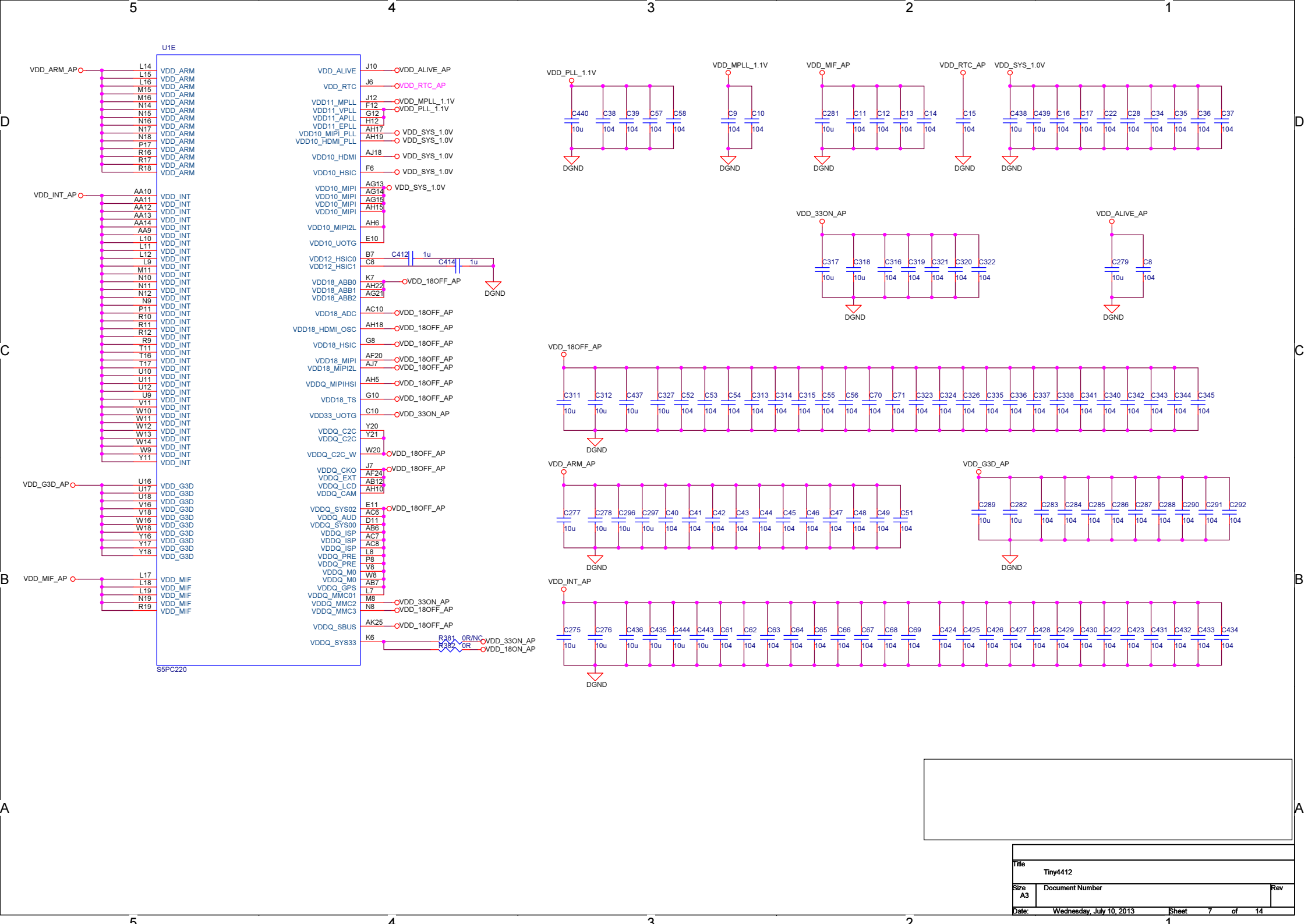


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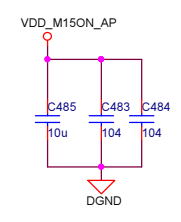
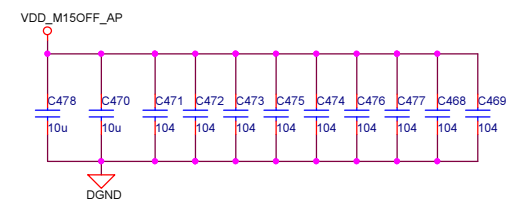
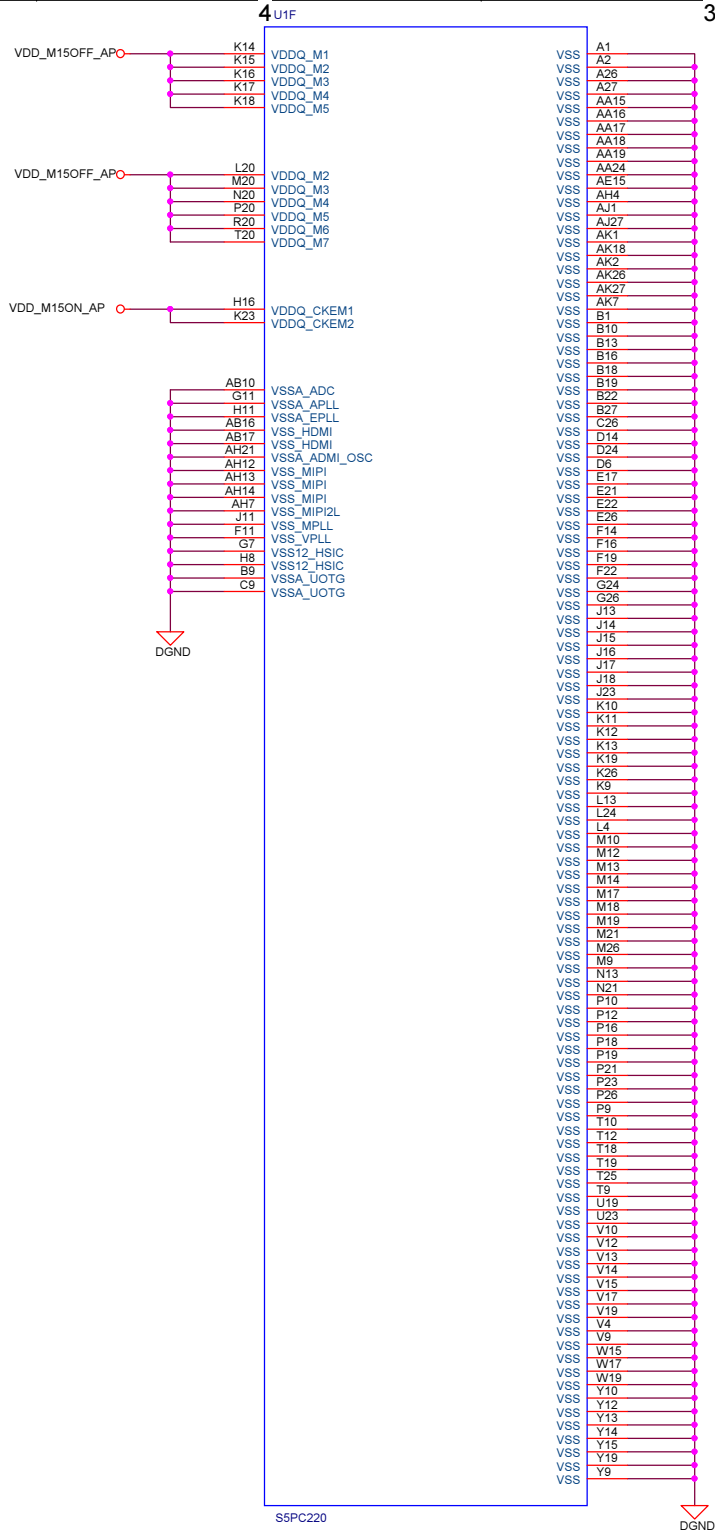


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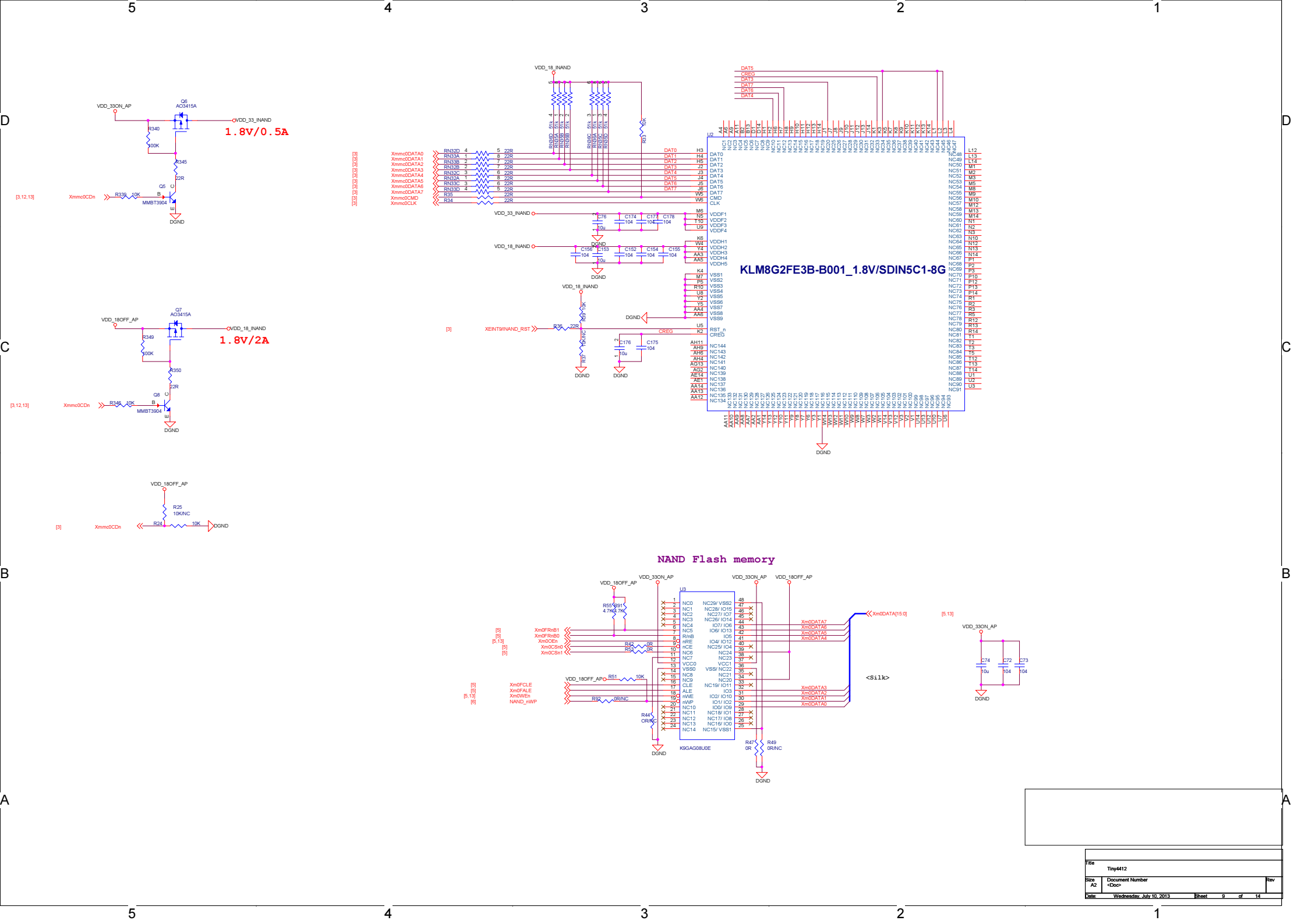


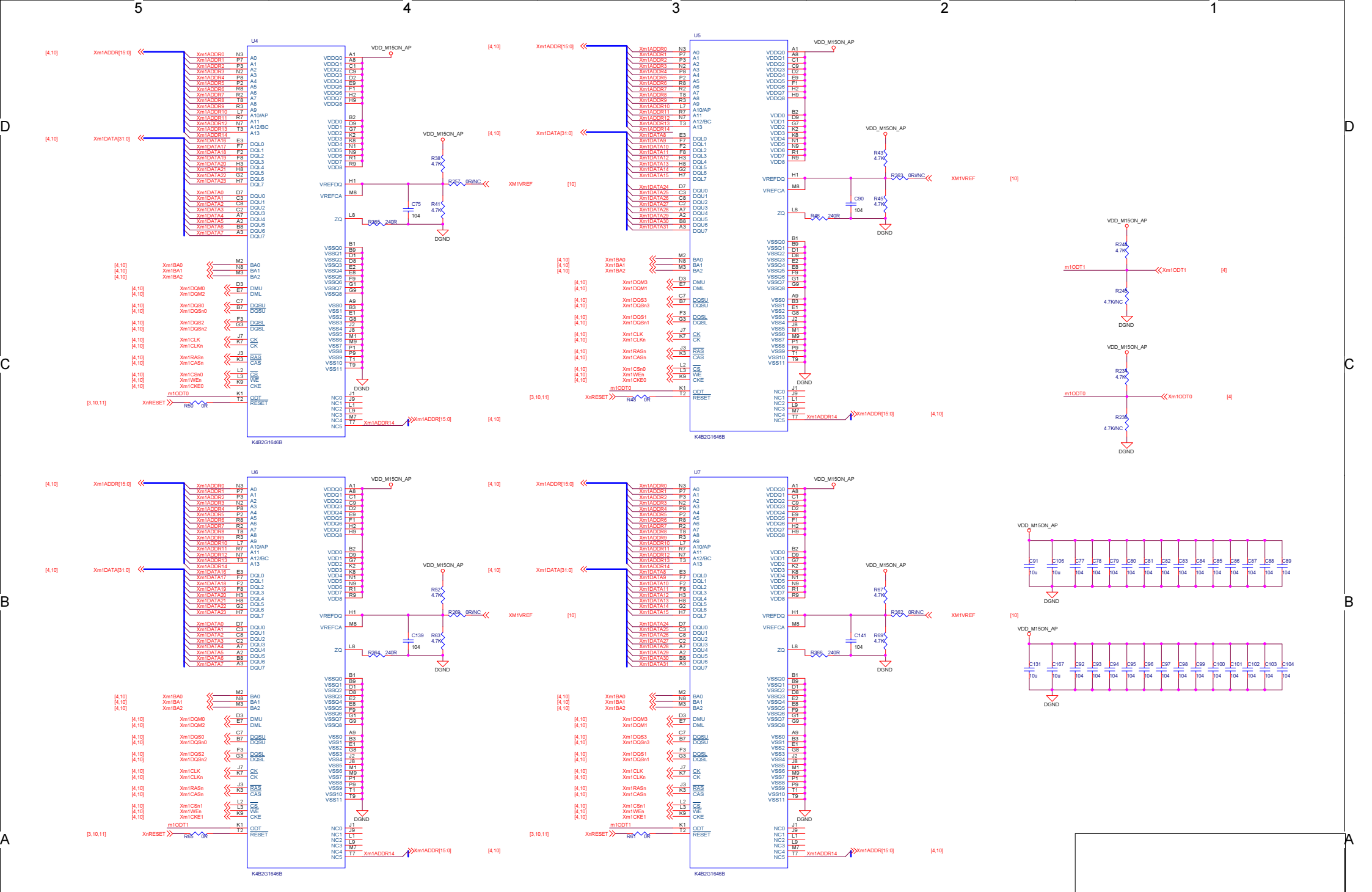


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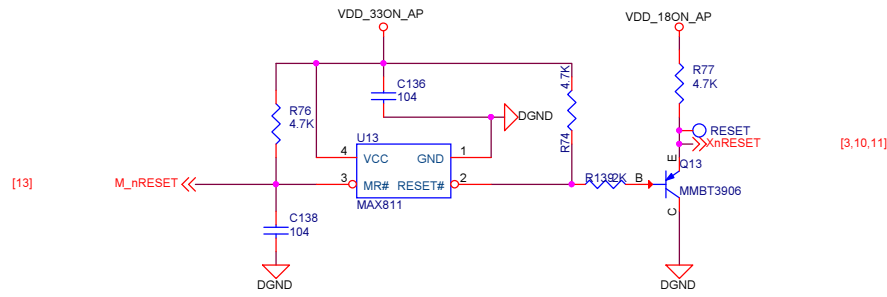


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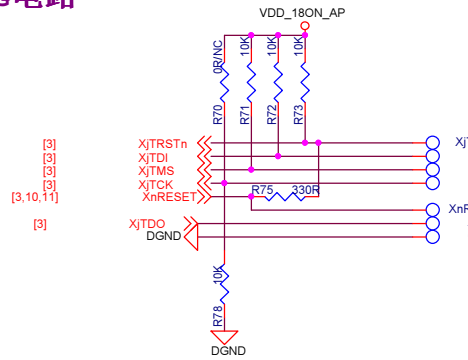




复位电路



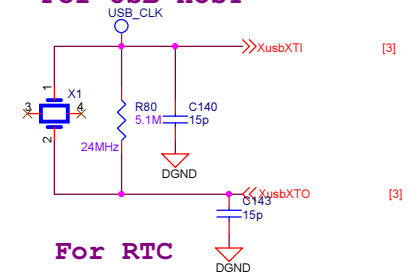
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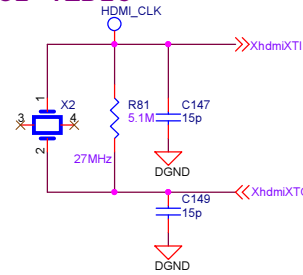
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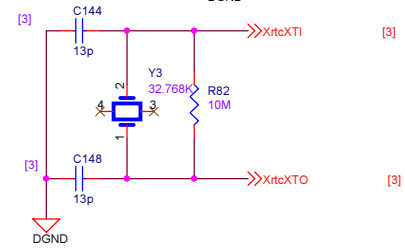
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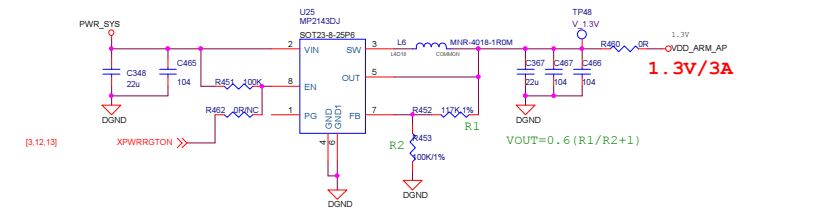
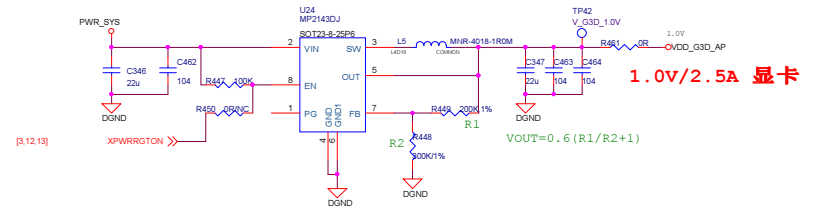
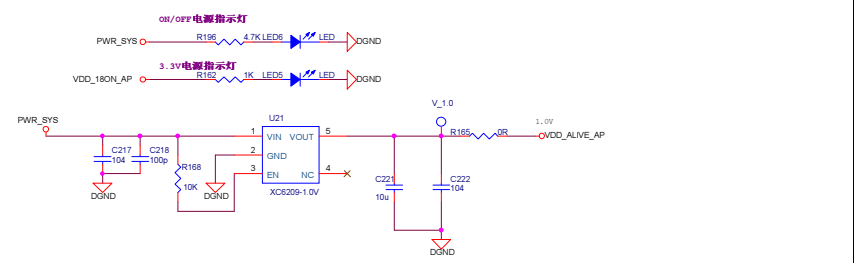
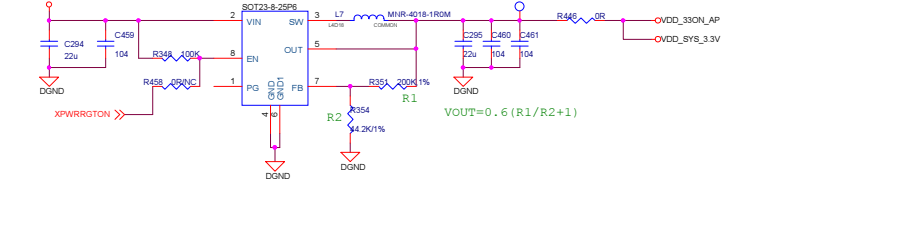
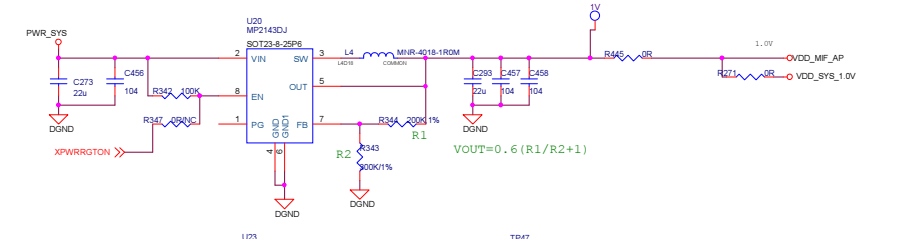
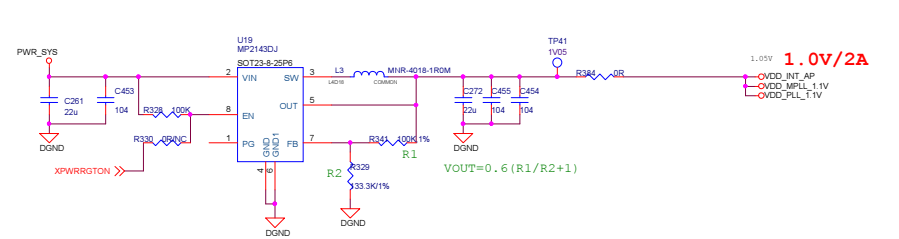
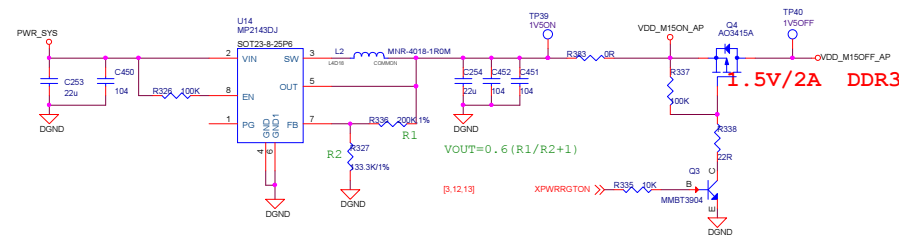
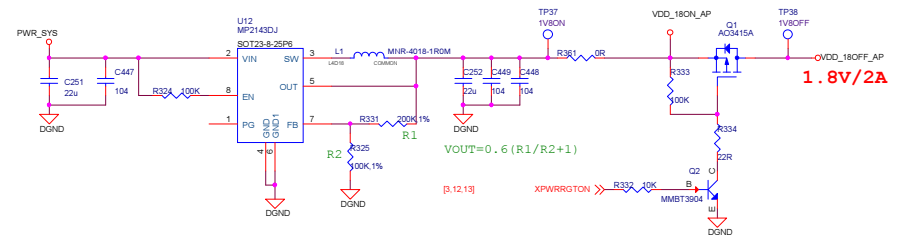
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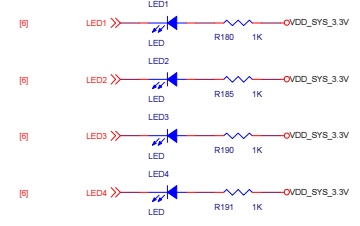
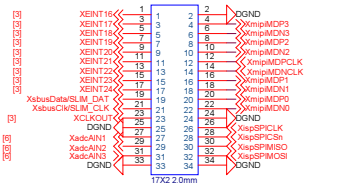
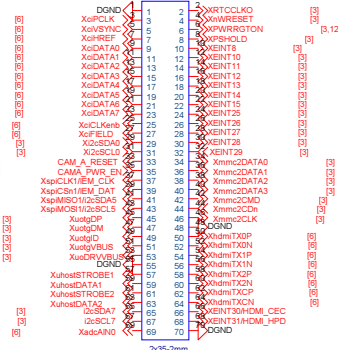
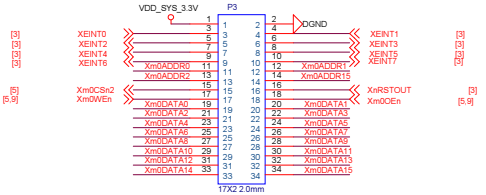
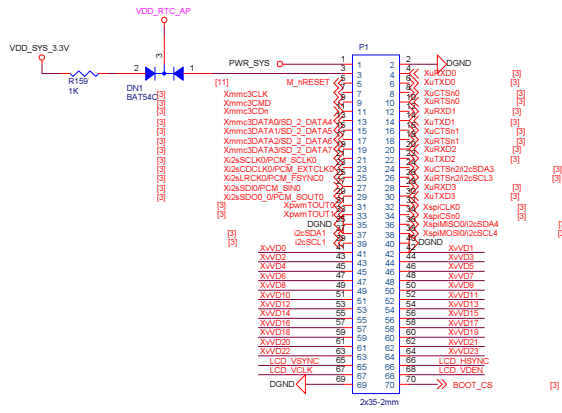
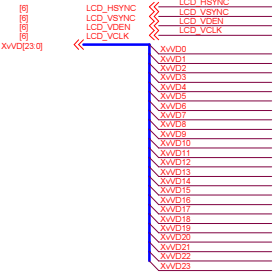
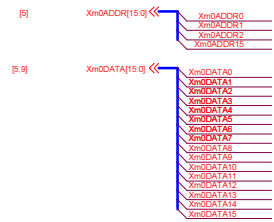
For RTC



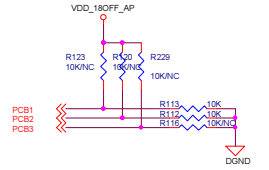
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PCB 版本控制



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ID1	ID2
ID3	ID4
ID6	ID5
定位点	

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