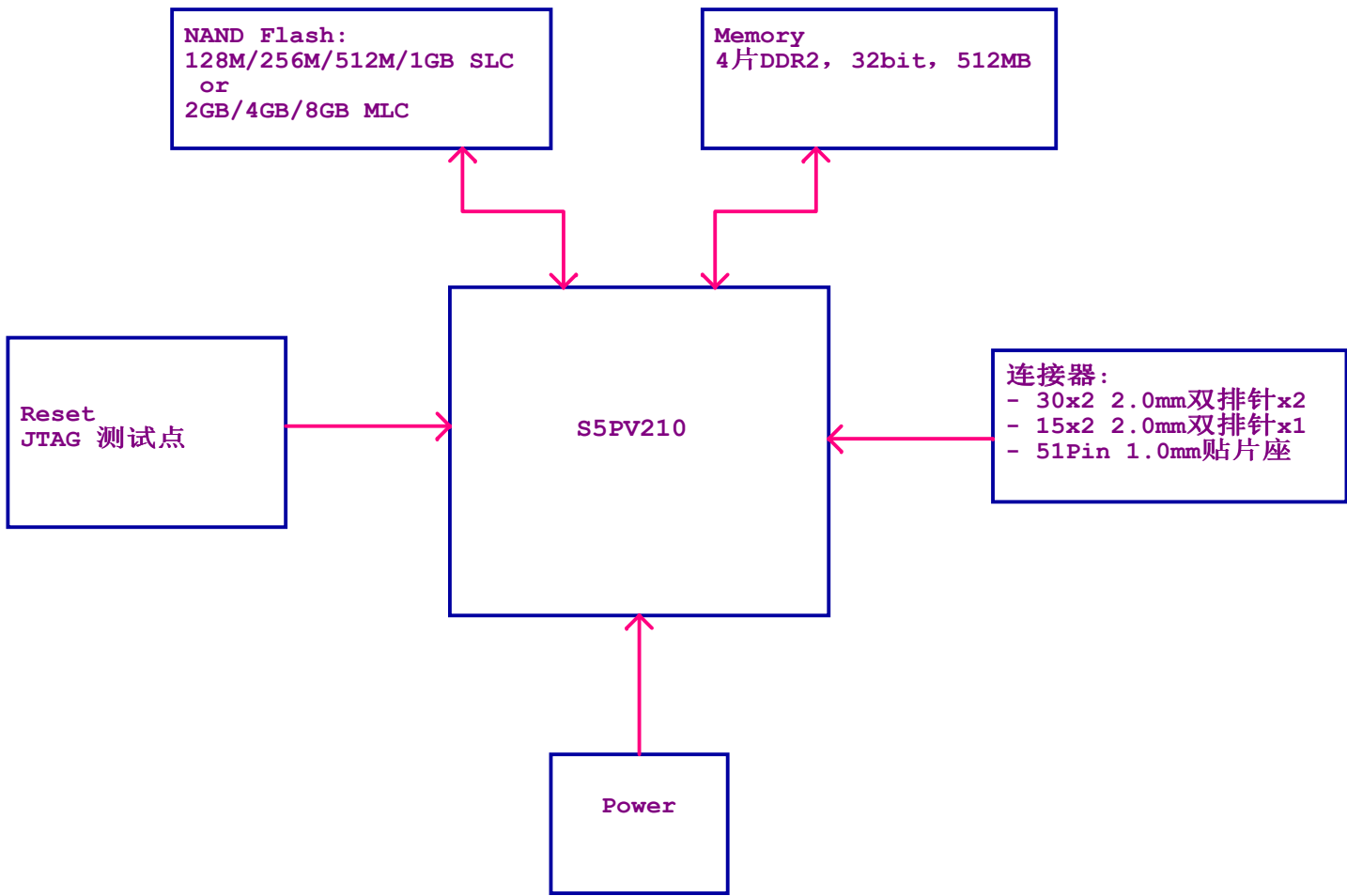


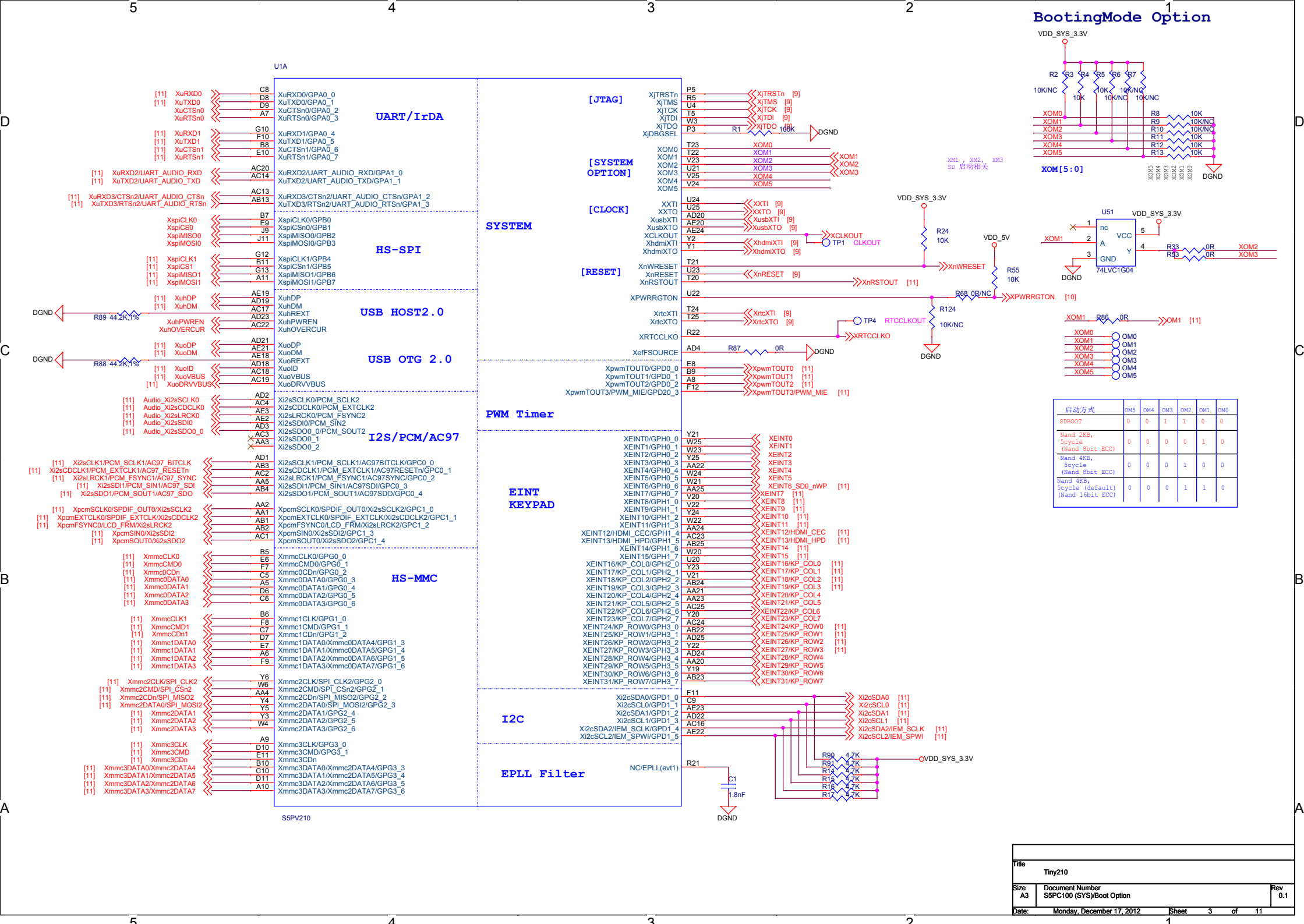
Revision History

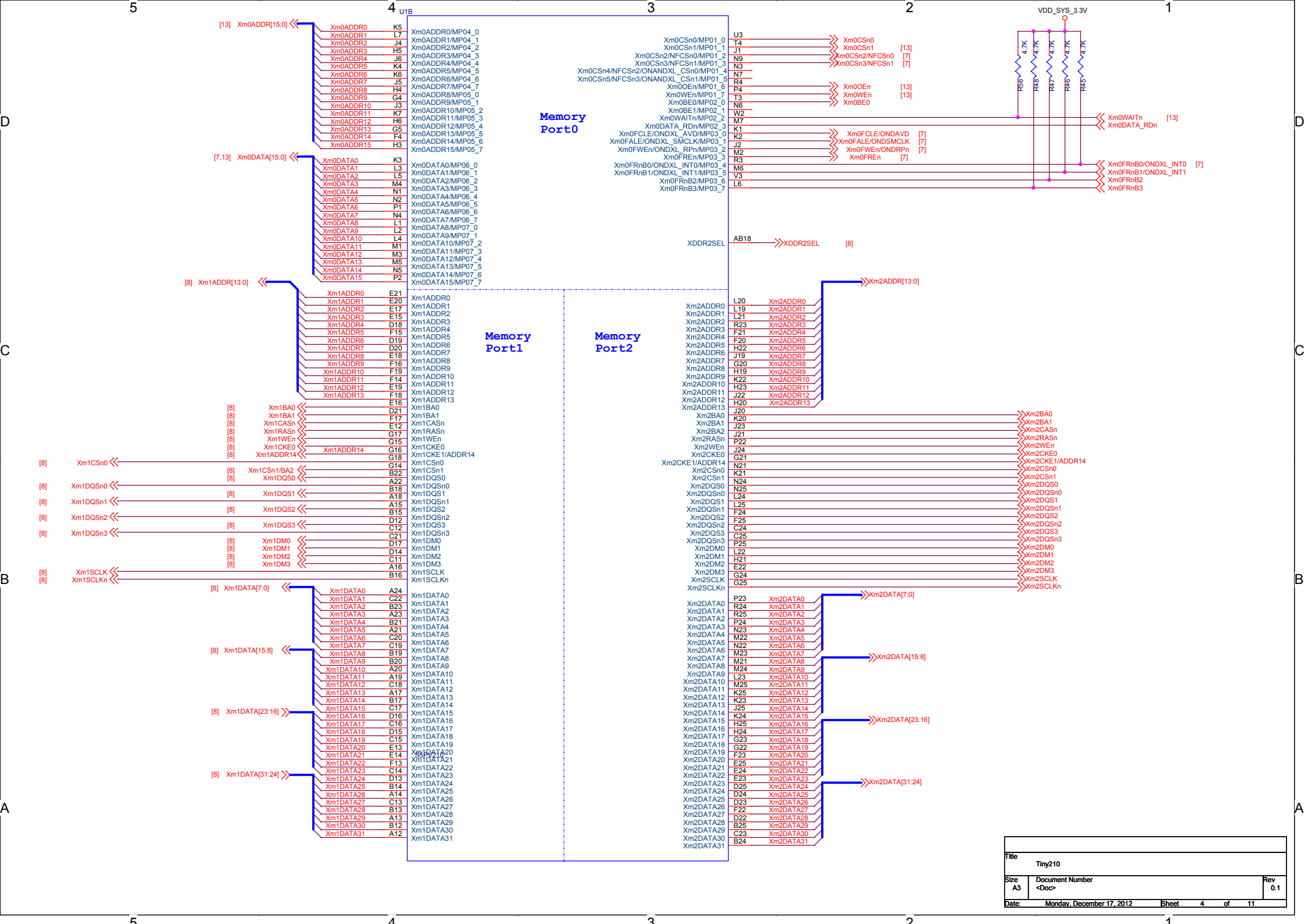
Revision	Date	Comments
1.0	2011-12-9	初次版本

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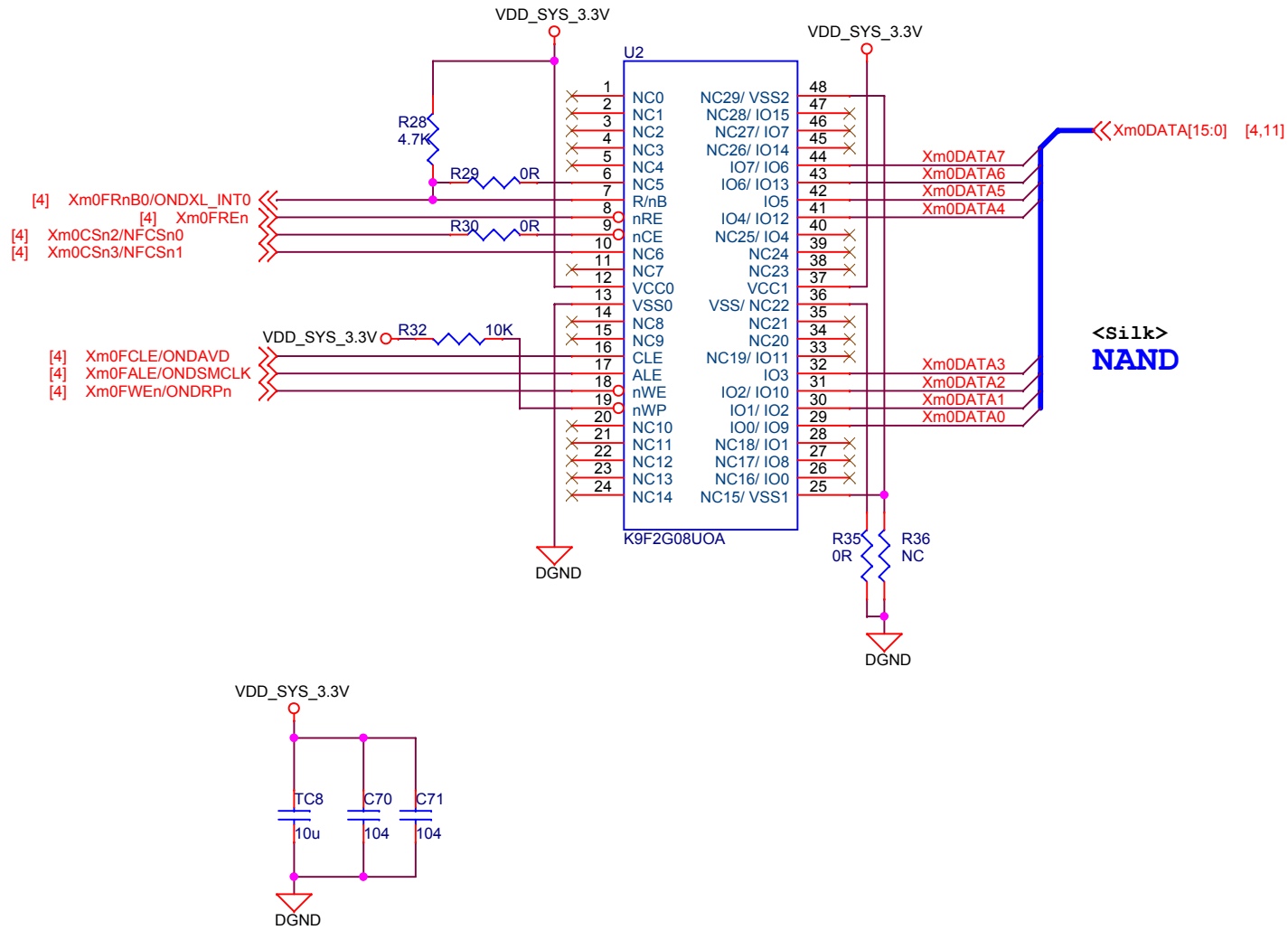






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NAND Flash memory

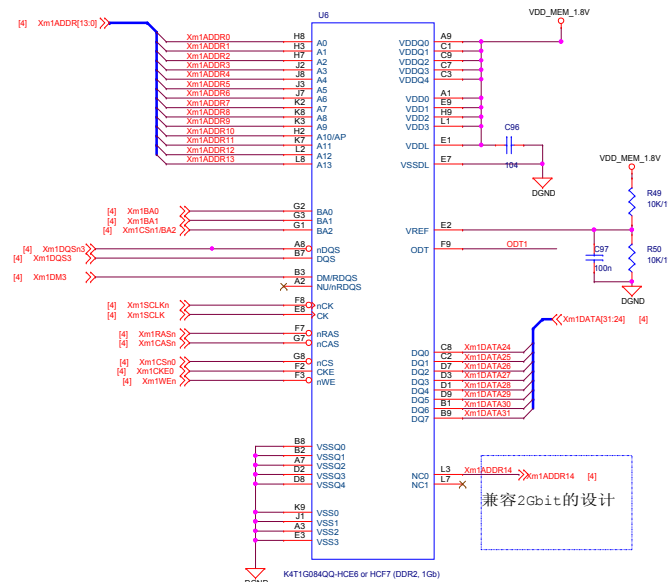
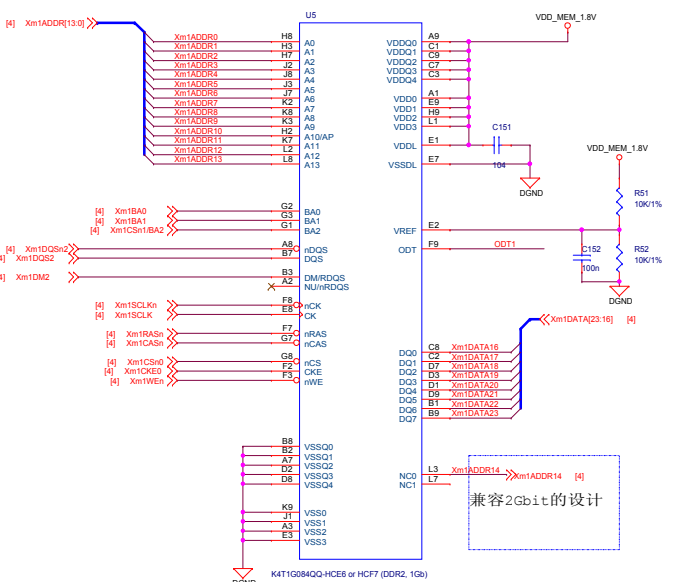
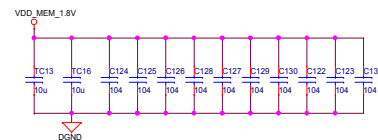
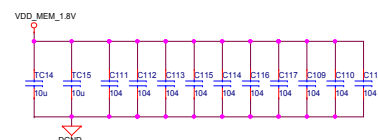
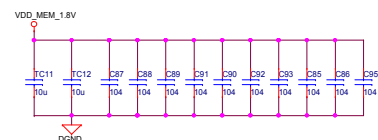
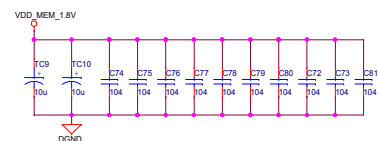
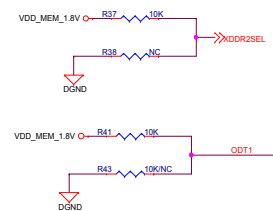
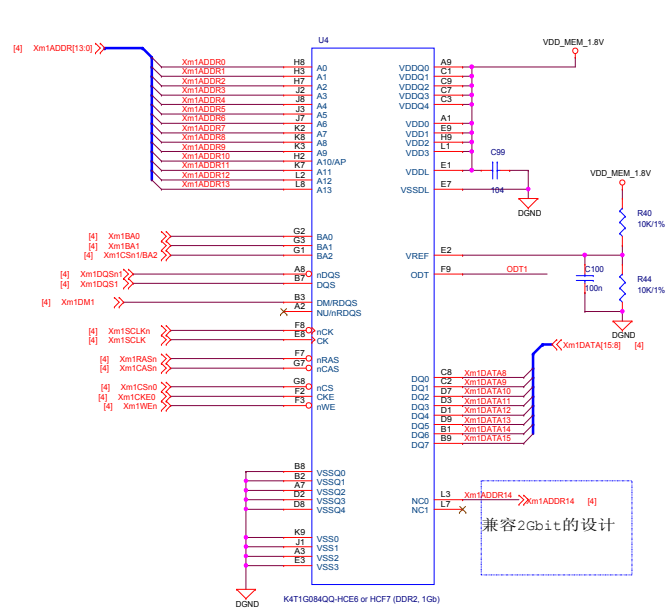
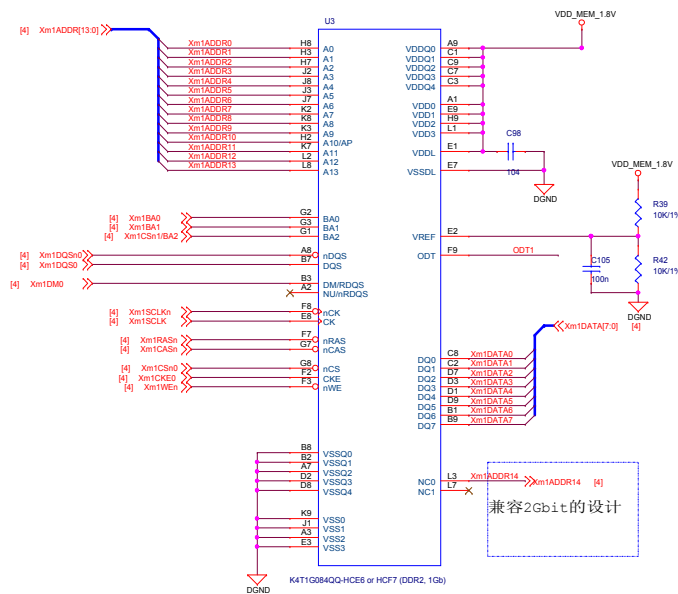


Title
Tiny210

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A4

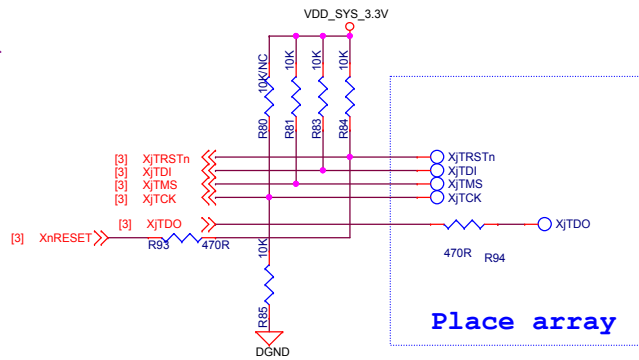
Document Number
<Doc>

Rev
0.1

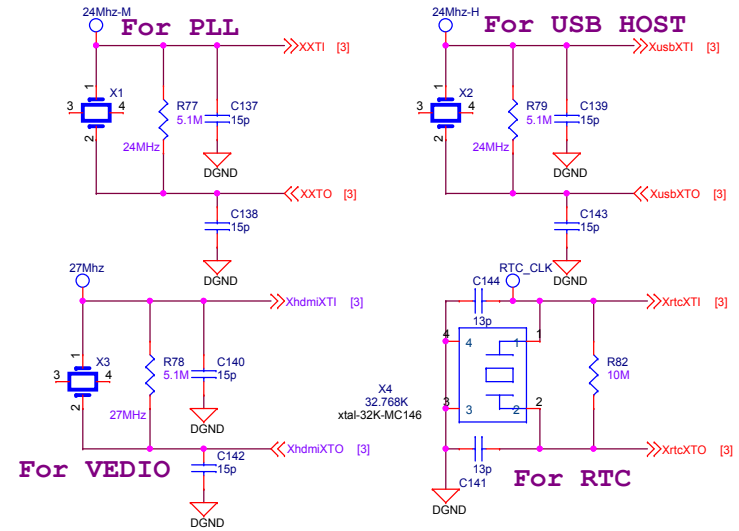
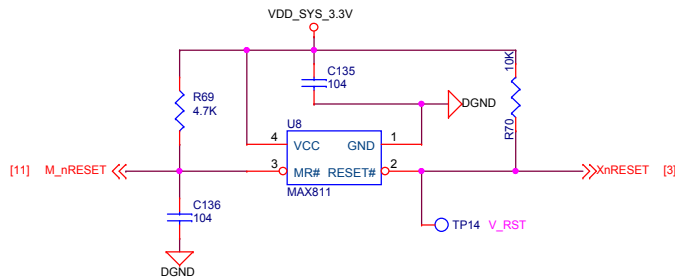


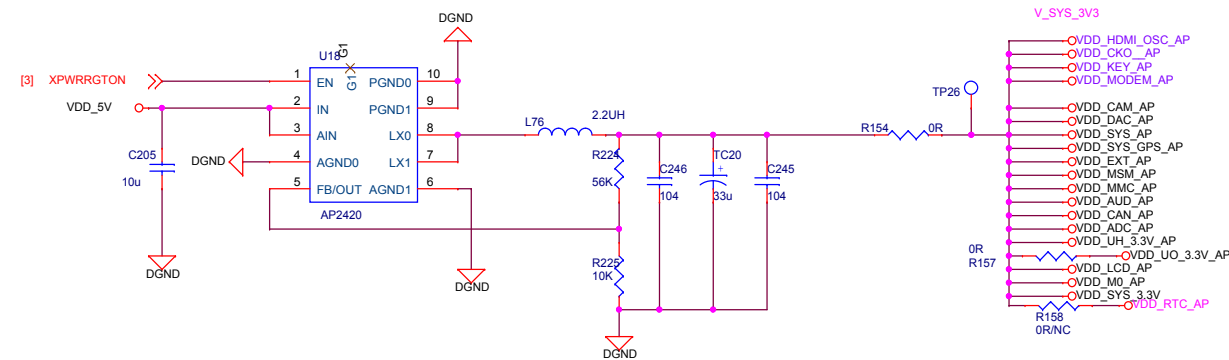
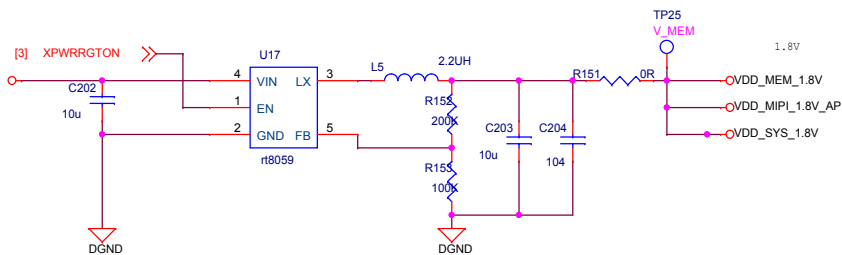
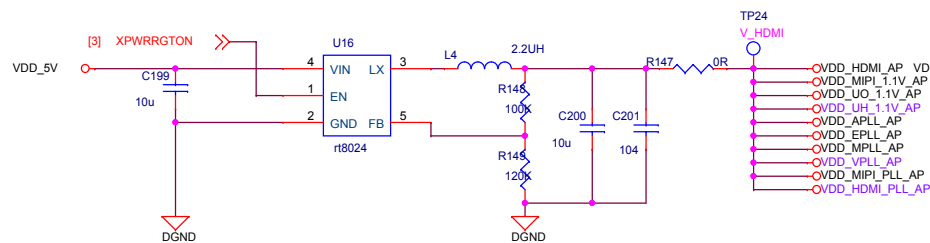
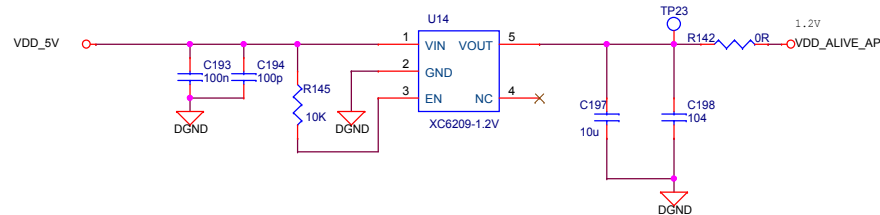
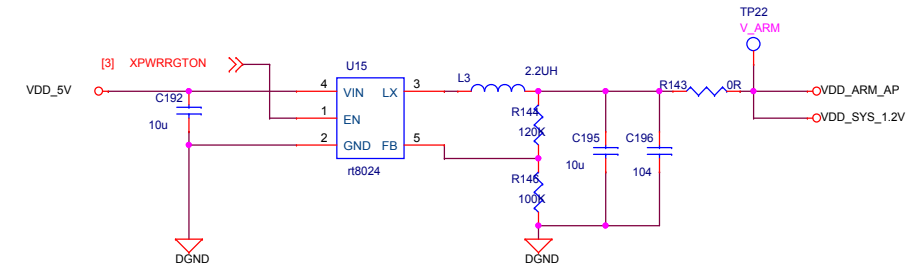
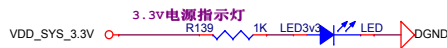
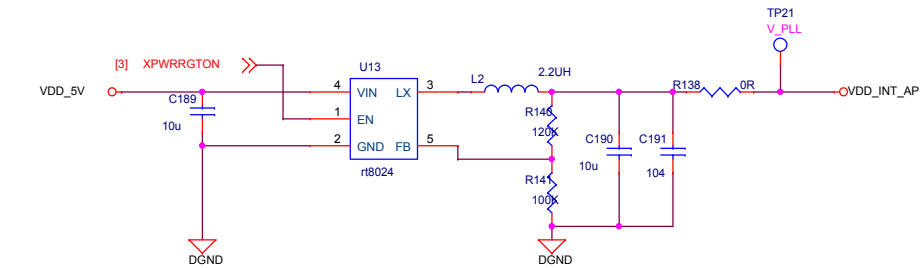
Title			
Tiny210			
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JTAG电路

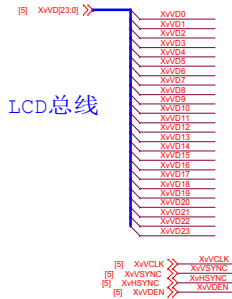
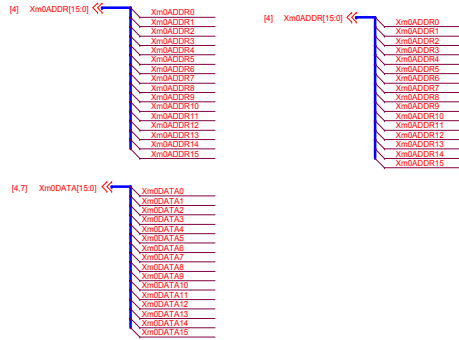


复位电路

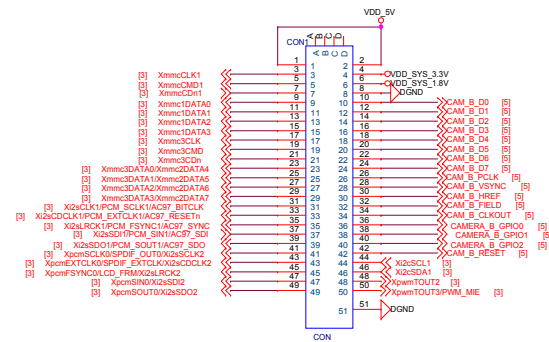
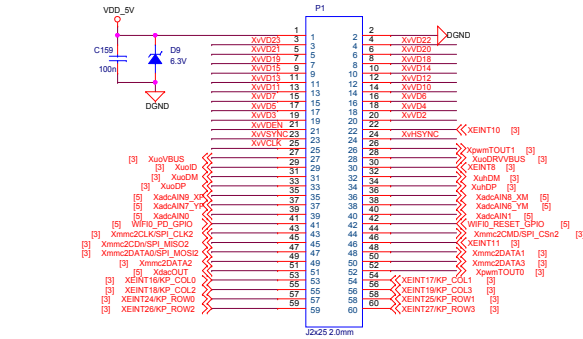
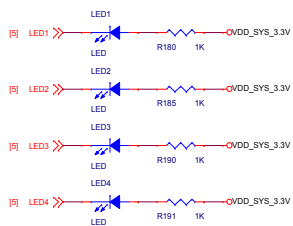




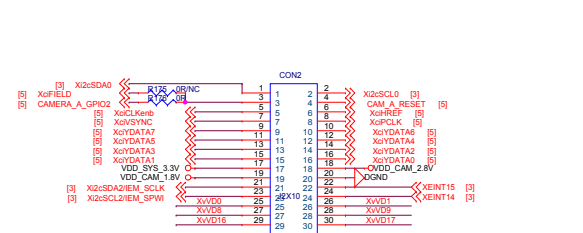
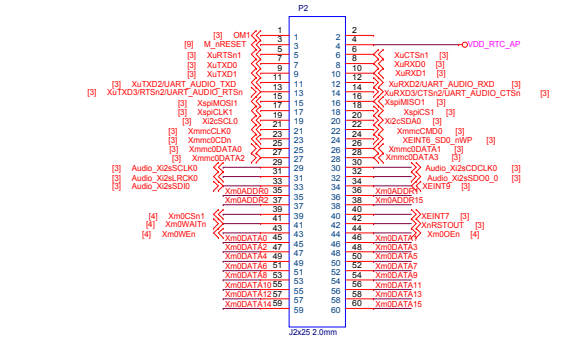
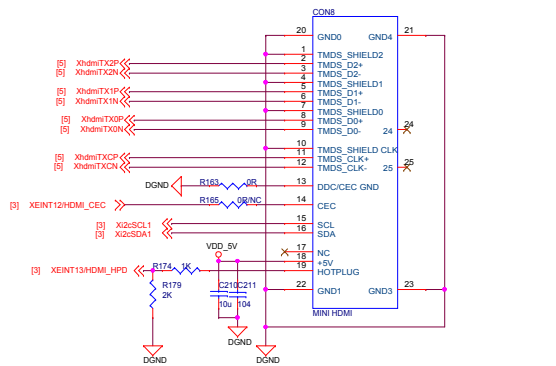
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LED x 4



MINI HDMI接口电路



改为贴片座，也是2.0mm间距的IDC连接器

