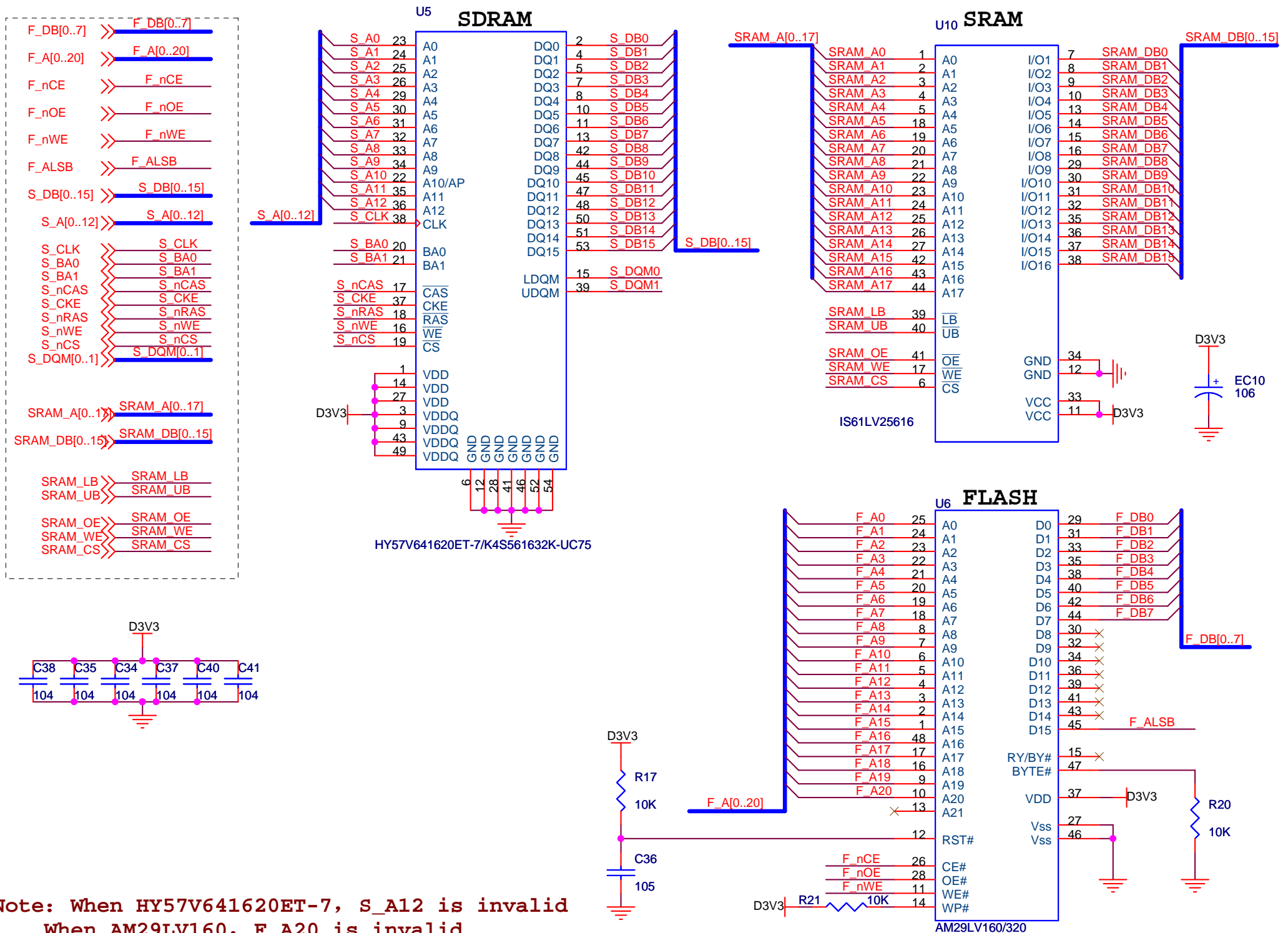


Num	Description	Num	Description
1	1_POWER	6	
2	2_INTERFACE	7	
3	3_FPGA	8	
4	4_FLASH_SDRAM	9	
5		10	

Niello Power Model: DB4CE15 2010-2011 @ Niello Power <i>Http://www.heijin.org</i>			
Date	2011.12.1	Verision	3.0



Note: When HY57V641620ET-7, S_A12 is invalid
When AM29LV160, F_A20 is invalid